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Posted Date: 30 May 2024

doi: 10.20944/preprints202405.2006.v1

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Article

A High Dimensional and High Sigma Statistical Model for SRAM Read Access Yield Estimation

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Abstract: High dimensional statistical analysis for the yield of large-scale circuits is quite difficult due to expensive simulations, especially for the memory circuits with high sigma requirement (e.g., SRAM). In this paper, we developed an efficient sparse additive model to substitute simulations. To fit high sigma region accurately, the modeling center is moved to near failure boundary searched by scaling the shape of sampling function. To solve the model efficiently, the process variables are grouped by standard cells so that the model can be solved by our developed blockwise greedy algorithm. The experiments on the 28nm memory circuits validate that our method achieves high accuracy and efficiency compared with other state-of-art works.

Keywords: SRAM; yield analysis; high sigma; high dimension

1. Introduction

The reliability of integrated circuits is severely affected by process variation as semiconductor technology continues to advance, especially for memory such as the Static Random-Access Memory (SRAM). The SRAM consists of millions of replicated bitcells and is more susceptible to the variation due to the minimized cell size. The failure rate of each SRAM cell should be extremely low ($10^{-8} \sim 10^{-5}$) to make sure the acceptable yield before taping out the chip.

SRAM yield can be measured by static stability metrics, such as static noise margin (SNM), and dynamic stability metrics, such as read access yield and write yield [1]. In this paper, we focus on read (access) yield because they can reflect the dynamical characteristics of SRAM under the assisted circuits and column leakage [1]. As a result, a large number of process variables should be considered for predicting yield, which forms a high dimensional variation space. For example, there are over ten thousand process variables to be considered for 256-bits column with sense amplifier. However, the methodology can be extended for other yield metrics.

To estimate such high-sigma “events” accurately, many statistical methods have been proposed. Among them, Standard Monte Carlo (MC) estimation is typically regarded as the gold reference, which samples the variation space directly and simulates each sample to get the corresponding response. However, MC based on circuit simulator, such as Spice, is extremely slow because it needs hundred millions of simulations to capture a single failure point.

2. Preliminaries

2.1. Problem Formulation

We consider $\mathbf{X} = (x_1, x_2, \dots, x_m)$ as a vector consisted of m independent normalized Gaussian variables modeled from process parameters. And $H(\mathbf{X})$ is the probability density function (PDF) of \mathbf{X} . Let Y be the circuit performance which can be measured through expensive transistor-level simulation, such as inverter chain delay, SRAM read access time etc.

For the failure rate evaluation of SRAM, S denotes the failure region. Typically, the set S is extremely small. We define the circuit performance doesn't meet the specification when $Y \in S$. And we further introduce indicator function $I(X)$ to identify pass/fail of Y :

$$I(X) = \begin{cases} 0, & \text{if } Y \notin S \\ 1, & \text{if } Y \in S \end{cases} \quad (1)$$

Therefore, the probability can be calculated as:

$$P_{fail} = P(Y \in S) = \int I(X) \cdot H(X) dX \quad (2)$$

Unfortunately, the formulation (2) is difficult to calculate analytically because we don't know what distribution $I(X)$ satisfies exactly. Traditionally, Monte Carlo is used to estimate the failure probability by sampling from $H(X)$ directly, and the unbiased estimate of P_{fail} :

$$\hat{P}_{fail} = \hat{P}(Y \in S) = \frac{1}{N} \sum_{i=1}^N I(x_i) \xrightarrow{N \rightarrow +\infty} P(Y \in S) \quad (3)$$

2.2. Importance Sampling

For estimating SRAM failure rate, $Y \in S$ is a high sigma "event". Standard MC needs hundred millions of expensive circuit simulations to capture such a rare "event". It is not realistic to apply MC to the practical design especially the circuit size is large because each sample needs a transistor-level simulation. To address this issue, IS methods have been introduced to sample near the failure region through a "distorted" sampling distribution $g(X)$.

By constructing the distorted sampling function $G(X)$, we can pick more failure samples. And the failure probability can be expressed as (4):

$$P_{fail} = P(Y \in S) = \int I(X) \cdot \frac{H(X)}{G(X)} \cdot G(X) dX \quad (4)$$

$$= \int I(X) \cdot w(X) \cdot G(X) dX \quad (5)$$

Here, the $w(X)$ denotes the likelihood ratio between original PDF $H(X)$ and the distort PDF $G(X)$ which compensates for the discrepancy between $H(X)$ and $G(X)$. And unbiased IS estimator $\hat{P}_{IS,fail}$ can be calculated as (6):

$$\begin{aligned} \hat{P}_{IS,fail} &= \hat{P}_{IS}(Y \in S) \\ &= \frac{1}{M} \sum_{j=1}^M w(x_j) I(x_j) \xrightarrow[M \rightarrow +\infty]{a.s.} P(Y \in S) \end{aligned} \quad (6)$$

With a proper $g(X)$, $\hat{P}_{IS,fail}$ can be approximately equal to MC results.

However, the likelihood ratio $w(x_j)$ shows huge numerical instability in the high dimensional scenarios. Some $w(x_j)$ become dominant and even make the estimation result infinite so that the Equation (6) becomes unreliable.

2.3. Meta Modeling

Meta model is another solution to accelerate yield analysis by substituting the expensive simulations. It enables simulation evaluations from minutes to hours to be achieved in microseconds so that the large scale MC is feasible.

Due to high dimensional input variables, the effective meta models are typically written as the additive forms:

$$f(X) = f_0 + \sum_{i=1}^m f_i(x_i) \quad (7)$$

where f_0 is the intercept used to measure the mean of response and $f_i(x_i)$ represents the effect of a single variable x_i acting independently upon Y .

Equation (3) shows the \hat{P}_{fail} heavily depended on the indicator function $I(X)$. And the circuit performance near the failure region predicted by meta model must be accurate enough to guarantee

the correct judgement of $I(\mathbf{X})$. Otherwise, it will lead to a large bias due to the error accumulation. However, the existing meta-model methods suffers from two problems: first, the accuracy fails to be maintained in the nonlinear region far away the original points. Second, Equation (7) cannot reflect the correlated effects brought by two related variables, which also lead to larger bias of estimation.

3. Proposed Method

To formulate our sparse additive model, we first pre-process the input variables as follows:

$$\mathbf{X} = (\mathbf{X}_L, \mathbf{X}_R) = (x_1, x_2, \dots, x_m) \quad (8)$$

where $\mathbf{X}_L = (l_1, l_2, \dots, l_{nl})$ is the nl process variables related to devices, such as threshold parameter V_{th0} , cutoff voltage V_{off} , and electron mobility u_0 . And $\mathbf{X}_R = (r_1, r_2, \dots, r_{nr})$ is the critical parasitic parameters of long bitlines extracted from the post layout SRAM array.

Notice that the input signal only walks through the critical path to generate the output of a circuit, which means large number of standard cells have few effects on the output. Hence, we group the variables by the standard cells. Suppose there are P cells in total, then the grouped variables can be represented as:

$$\mathbf{X}_L = \{\mathbf{L}_1, \mathbf{L}_2, \dots, \mathbf{L}_P\} \quad (9)$$

where $\mathbf{L}_i = (l_{i1}, l_{i2}, \dots, l_{ici})$ $i = 1, \dots, P$. They are ci -dimensional vectors from the i th cell. \mathbf{L}_p is the extracted post-layout parasitic parameters such as equivalent resistor-capacitance (RC) to ground of bitline and wordline.

Based on these processed input variables, we formulate our meta as follows:

$$f(\mathbf{X}) = \beta_0 + \sum_{i=1}^P \mathbf{B}_i \Phi_i(\mathbf{L}_i) + \mathbf{C} \Psi(\mathbf{X}_R) \quad (10)$$

$$s.t. \mathbf{B}_i \Phi_i(\mathbf{L}_i) = \sum_{j=1}^N w_{i,j} \varphi_{i,j}(\mathbf{L}_i) + \sum_{j=1}^{ci} \beta_{i,j} s(l_{i,j}) \quad (11)$$

$$\mathbf{C} \Psi(\mathbf{X}_R) = \sum_{j=1}^N w_j \varphi_j(\mathbf{X}_R) + \sum_{k=1}^{nr} c_k s(r_k) \quad (12)$$

where β_0 is the intercept to represent the modeling center. Φ_i is the i th basic function set of the grouped \mathbf{L}_i and $\mathbf{B}_i = (w_{i,1}, \dots, w_{i,N}, \beta_{i,1}, \dots, \beta_{i,ci})$ is the i th grouped coefficient vector. Ψ is the basic function set of \mathbf{X}_R and $\mathbf{C} = (w_1, \dots, w_N, c_1, \dots, c_{nr})$ is the coefficient vector. $\varphi_{i,j}(\mathbf{L}_i)$ is the multivariable gaussian radial basic function (RBF) interpolation [10] of \mathbf{L}_i to capture the nonlinear effects of process variables in the i th cell and $w_{i,j}$ is the corresponding weights. $s(l_{i,j})$ is the polynomial function of j th variable of \mathbf{L}_i . N is the number of training samples. φ_j is the RBF interpolation of parasitic parameter \mathbf{X}_R and $s(r_k)$ is the polynomial function of k th variable of \mathbf{X}_R .

For Equation (10), there are large number of coefficients to be solved. And we notice that the sparsity is reflected not only between groups but also within the group. Because except that most cells that are not in the critical path do not affect circuit performance, most devices in a cell located in the critical path also have few effects on the circuit performance. To full exploit the sparsity from inter groups and intra group, we can formulate the objective function as follows.

$$Q(\Omega) = \frac{1}{2N} \|Y - \sum_{i=1}^P \mathbf{B}_i \Phi_i(\mathbf{L}_i) - \mathbf{C} \Psi(\mathbf{X}_R)\|_2^2 + (1 - \alpha) \lambda \sum_{i=1}^P \|\mathbf{B}_i\|_2 + \alpha \lambda \|\Omega\|_1 \quad (13)$$

where $\Omega = (\mathbf{B}_1, \dots, \mathbf{B}_P, \mathbf{C})$ is the entire parameter space; $\alpha \in [0 \sim 1]$ and λ are regularization constants; α decides the balances of the convex combination of L1 and L2 norm penalties and λ controls the degree of sparsity of the solution; N is the number of samples. Since the β_0 doesn't need to be penalized, we omit it from the objective function and calculate it when the other coefficients convergence.

The form of objective function is simplified to a sparse group lasso problem, which can be solved by the modified blockwise greedy algorithm efficiently.

The overview of the yield analysis method based on our model, named SAIM, is summed as Algorithm 1. The training samples generated by the distorted sampling function $g(\mathbf{X})$ located on the failure boundary by our modeling center search method. After constructing our model, we generate

enough number of MC samples and evaluate them by SAIM to obtain the yield prediction according to Equation (3).

Algorithm 1: SAIM Model Based Yield Analysis

Input: The sample set $(\mathbf{X}_i, \mathbf{y}_i)$, $i = 1, \dots, N$ from the $H(\mathbf{X})$

Output: The SRAM failure rate \hat{P}_{fail}

1. Modeling center search

Finding the failure region globally by scaled the shape of $H(\mathbf{X})$ and initialize the distorted sampling function $G(\mathbf{X})$ to generate training set.

2. Model Construction

2.1 Generate the training samples from $G(\mathbf{X})$.

2.2 Group the variables and construct the SAIM model:

$$f(\mathbf{X}) = \beta_0 + \sum_{i=1}^p \mathbf{B}_{li} \Phi_{li}(\mathbf{L}_i) + \mathbf{C} \Psi(\mathbf{X}_R)$$

2.3 Using our blockwise greedy solver to solve the model coefficient.

3. Failure Rate Calculation

Calculate the failure rate as:

$$\hat{P}_{fail} = \frac{1}{N} \sum_{i=1}^N I(f(\mathbf{X}) \in S)$$

4. Implement Details

4.1. High Dimensional Failure Boundary Search

In the deep submicron technology node below 65nm, the process variables have strong nonlinear effects on the circuit performance [11] due to the smaller device size and lower operation voltage.

In the yield estimation scenario, only the accuracy near the failure boundary will be concerned. As illustrated in Equation (3), the error will be accumulated when indicator function $I(\mathbf{X})$ has a wrong judgement. As a result, the closer training samples are to failure boundary, the more computational cost will be saved.

Directly fit the entire nonlinear region needs too large number of samples, especially for the samples far away the original points. To guarantee the accuracy in the entire region, we have to obtain the samples by its original distribution to avoid the “covariate shift” [13] that leads to large bias on the parameter estimation. Besides, the modeling efforts will increase drastically due to the needs of constructing high-order basic functions to fit the nonlinear region far away the original point.

The failure boundary is very difficult to search in the high dimension space for the too complicated shape. The Euclidean distance (or the L2-norm) of two high dimensional samples is almost same [12], which means the search method [4] based on minimizing the L2-norm fails in the high dimensional scenario.

We define Σ is the covariation matrix of \mathbf{X} , which has been normalized as identity matrix. The Σ determines the shape of sampling function $H(\mathbf{X})$. We enlarge the Σ by multiplying the scale parameter r to generate 1000 samples from $H(\mathbf{X})$ with $r\Sigma$, $H(\mathbf{X}|r\Sigma)$.

As shown in Figure 1, with the increased r , the failure samples are more easily obtained. Suppose N failure samples have been collected, we can construct a new sample function as follows:

$$G(\mathbf{X}) = \frac{1}{N} \sum_{i=1}^N q_i(\mathbf{X}|\mathbf{u}_i, \Sigma) \quad (12)$$

where the \mathbf{u}_i is the i th failure sample represented by a vector. And $q_i(\mathbf{X}|\mathbf{u}_i, \mathbf{\Sigma})$ is the multi-dimensional Gaussian distribution with mean \mathbf{u}_i and covariation matrix $\mathbf{\Sigma}$. Then training set will be generated by $G(\mathbf{X})$.

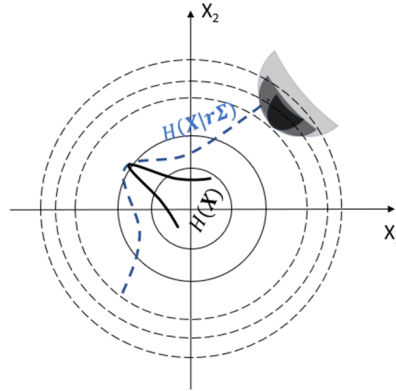


Figure 1. Scaled Shape Sampling.

4.2. Blockwise Model Solver

The derived objective function in Equation (11) is not a continuously differentiable function. The typically used Least Squares, Gradient Descent and Newton's method will not work. In this work, we developed the strategy of block coordinate descent (BCD) [14] to solve parameters efficiently. It selects the critical groups and iteratively minimizes the objective function in each group coordinate while the other coordinates are fixed. The solver is summarized in Algorithm 2.

Algorithm 2: Blockwise Model Solver

Input: The sample set $(\mathbf{X}_i, \mathbf{y}_i)$, $i = 1, \dots, N$ generated by the constructed $G(\mathbf{X})$

Output: The model parameter $\mathbf{\Omega} = (\mathbf{B}_1, \dots, \mathbf{B}_P, \mathbf{C})$

1. Initialization

1.1. $\mathbf{B}_1, \dots, \mathbf{B}_P, \mathbf{c}_1, \dots, \mathbf{c}_{nr} \leftarrow \mathbf{0}$

1.2 Set iteration $\mathbf{t} = \mathbf{0}$

1.3 The residual is:

$$\mathbf{R}^* = \mathbf{y} - \sum_{i=1}^P \mathbf{B}_i \Phi_i(\mathbf{L}_i) - \mathbf{C} \Psi(\mathbf{X}_R)$$

2. Repeat

2.1 for $\mathbf{k} = 1, \dots, P$

Update \mathbf{B}_k

2.2 $\mathbf{R} \leftarrow \mathbf{R}^* + \mathbf{B}_k^{(t)} \Phi_k(\mathbf{L}_k)$

2.3 $\mathbf{B}_k^{(t+1)} \leftarrow \arg \min_{\mathbf{B}_k} \frac{1}{2N} \|\mathbf{R} - \mathbf{B}_k \Phi_k(\mathbf{L}_k)\|_2^2 + (1 - \alpha) \lambda \sum_{i=1}^P \|\mathbf{B}_i\|_2 + \alpha \lambda \|\mathbf{\Omega}\|_1$

2.4 $\Delta = \mathbf{B}_k^{(t+1)} \Phi_k(\mathbf{L}_k) - \mathbf{B}_k^{(t)} \Phi_k(\mathbf{L}_k)$

2.5 $\mathbf{R}^* \leftarrow \mathbf{R}^* + \Delta$

2.6 endfor

Update \mathbf{C}

2.8 $\mathbf{R} < -\mathbf{R}^* + \mathbf{C} ? (\mathbf{X}_R)$

$$2.9 \quad \mathbf{C} \leftarrow \arg \min_{\mathbf{C}} \frac{1}{2N} \|\mathbf{R} - \mathbf{C} \Psi(\mathbf{X}_R)\|_2^2 + \alpha \lambda \|\mathbf{C}\|_1$$

$$2.10 \quad \Delta = \mathbf{C}^{(t+1)} \Psi(\mathbf{X}_R) - \mathbf{C}^{(t)} \Psi(\mathbf{X}_R)$$

$$2.11 \quad \mathbf{R}^* \leftarrow \mathbf{R}^* + \Delta$$

$$2.12 \quad \mathbf{t} = \mathbf{t} + 1$$

Until satisfy the convergence:

$$\frac{|Q^{(t)}(\Omega) - Q^{(t-1)}(\Omega)|}{Q^{(t-1)}(\Omega)} \leq \varepsilon$$

3. Compute the intercept β_0

$$\beta_0 = \text{average}(\mathbf{y} - \sum_{i=1}^P \mathbf{B}_i \Phi_i(\mathbf{L}_i) - \mathbf{C} \Psi(\mathbf{X}_R))$$

The ε is the specific convergence threshold. α and λ are regularization constants, which can be optimized by cross-validation [15].

In the step 2.3 and step 2.9, we optimize the coefficients of our basic functions. Our basic function consists of the RBF interpolation and polynomial to capture the local features and the global features of strong nonlinear curve, respectively. And the RBF interpolation has the following form:

$$\sum_{j=1}^N w_{i,j} \varphi_{i,j}(\mathbf{L}_i) = \sum_{j=1}^N w_{i,j} e^{(-\varepsilon^2 \|\mathbf{L}_i - \mathbf{L}_{i,j}\|^2)} \quad (12)$$

where $\|\mathbf{L}_i - \mathbf{L}_{i,j}\|$ is the L2-norm of $(\mathbf{L}_i - \mathbf{L}_{i,j})$ which can be viewed the radius of RBF. The RBF interpolation consists of N weighted RBF centered N samples which can accurately fit the local feature of an arbitrary curve. However, the RBF interpolation has two drawbacks: the convergence speed is slow when N is large because the weights w are obtained by calculating N^2 number of $e^{(-\varepsilon^2 \|\mathbf{L}_i - \mathbf{L}_{i,j}\|^2)}$, and the evaluation is instability when the radius is large because the L2-norm of two vectors will be much similar with increased dimension, which is also known as “curse of dimension” in measuring distance.

To address the first issue, we notice that it needs large number of samples to use RBF interpolation to fit the entire curve directly. We combine low-order polynomial to fit the curve globally and the RBF interpolation to fix locally to decrease N . In addition, we vectorize the RBF calculation so that we can utilize the special math kernel library (MKL) [16] embedded in MATLAB to calculate very fast with several milliseconds (In our experiment, we obtained over 1000x speedup compared with calculating weights by $N \times N$ nested loop). To address the second issue, we separate the parameter optimization of RBF interpolation and polynomial. We first solve the polynomial with L1 penalty and then we filter out the variable with zero coefficient to decrease the dimension of \mathbf{L} . If all the variables in a cell have no effects on SRAM performance, the entire grouped coefficients can be set “0” directly.

5. Experiment Result

The proposed SAIM based yield analysis method will be verified on the logical circuit path and a SRAM column, which both of them are frequently used in circuit design. And we extract the three process variables from PDK: Vth0_mis, u0_mis, Voff_mis, which represent the threshold mismatch, the electron mobility mismatch and cut-off voltage mismatch respectively. The parasitic parameters are directly extracted from post-layout circuits. Besides, we also implement OMP [7], LRTA [8] for model comparison and SSAIS [2], HDIS [3] for yield estimation comparison. All experiments are performed with MATLAB and HSPICE with 28nm TSMC model on the Server with Intel Xeon Gold 5118 CPU @ 2.30 GHz.

5.1. SRAM Path

A simplified SRAM path with sense amplifier (SA) is shown in Figure 2. The read operation begins by activating the first word-line (WL1) and the pre-charged bit-lines. One bit-line BL will

discharge through the first accessed cell and enlarges the voltage difference between BL and BLB. The read (access) delay is defined as the time required to generate the voltage difference between two bit-lines that can be sensed by the sense amplifier. Notice that to generate the worst case for the read operation, the accessed Bit cell 1 stores "0" and other idle cells store "1", which maximum the leakage current through idle bits to increase the read access delay and impede the successful read. We consider SRAM read delay as the performance metric, which the failure event happens when the read delay is beyond the specified threshold (i.e., the enable time of WL1).

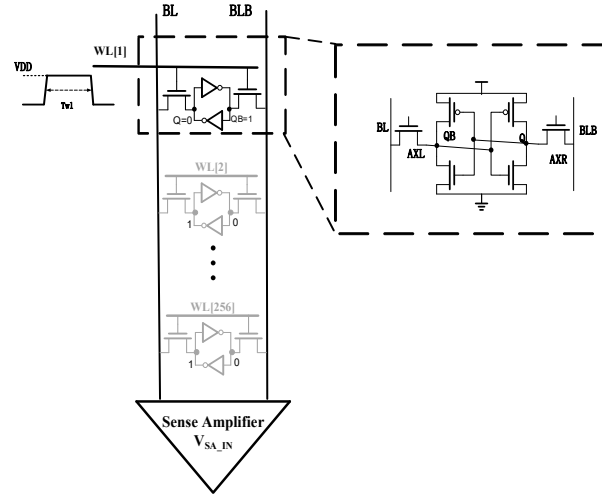


Figure 2. The simplified SRAM read path with sense amplifier.

5.2. Model Comparison

Our blockwise solver efficiency was first validated by comparing the convergence of our model with different configureures: the model with our blockwise solver and the model with generalized coordinate descent (GCD)[17]. The training set and test set were generated by $G(X)$. The simulations run parallel on the 10 cores Server. As shown in Figure 3, the model with blockwise solver quickly converges to around 3% relative error within 600 samples. While the model with GCD converges to around 8% relative error with 1200 samples.

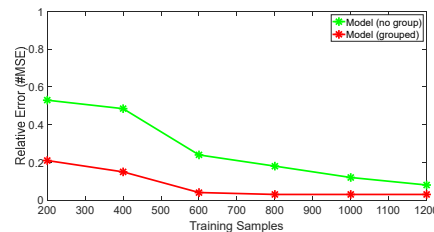


Figure 3. The training cost of model (grouped) v.s model (no group).

We also compared our model with the OMP and LRTA on different voltages with 1000 training samples. Both of them are high dimensional models. As shown the right original bars in Figure 4, our model converges to the relative error of around 3~5% on different voltages. While the accuracy of OMP and LRTA show high difference on different voltages. As the left blue bar and red bar shown in Figure 4, the relative errors of OMP and LRTA on 0.55V are up to 78% and 83%, respectively. The OMP and LRTA have a relative large bias compared with our model. It is because OMP and LRTA just are polynomial-based meta model. The basic functions of are polynomial which failed to capture the local features of the strong nonlinear curve. To illustrate it, we consider a univariate polynomial $F(x)$. The basis of $F(x)$ is just (x, x^1, \dots, x^n) where n is the order of $F(x)$ and the maximum number of coefficients of $F(x)$ is n , which means n samples are enough to solve $F(x)$ and extra samples are useless. As a result, $F(x)$ can only fit the curve globally due to its limited components. Figure 5 shows the sorted read delay and predicted results of different basic functions when

considering a single variable V_{th0} . As the black dot shown in Figure 5a), the read delay shows strong nonlinearity on 0.55V and 2~5 order polynomial failed to fit this curve. While in Figure 5b), the 4~5 order polynomial can fit the slightly nonlinear curve accurately under 0.7V. The accuracy of OMP and LRTA has improved greatly on high voltage.

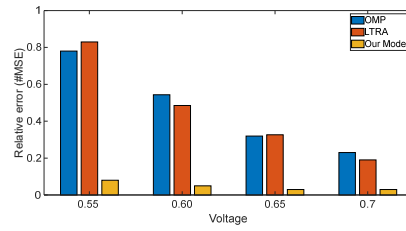


Figure 4. Accuracy comparison on different voltages.

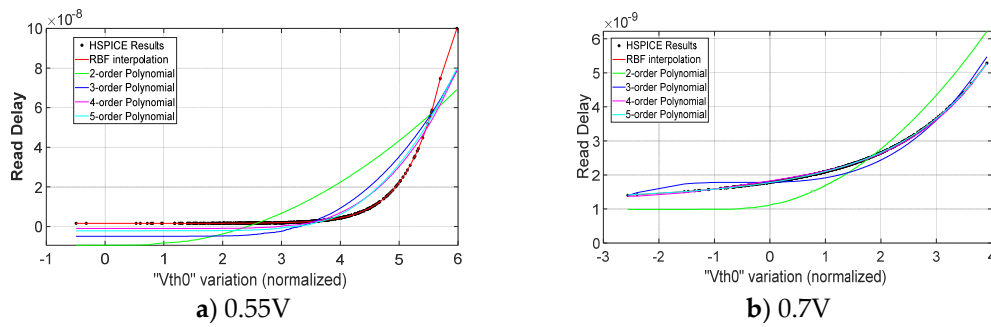


Figure 5. The fitting effects of different basic functions on different voltages.

Our model shows high accuracy across all voltages. It is because we use RBF interpolation to fit the nonlinear curve. The one-dimensional RBF interpolation has the form of $\sum_{i=1}^N w_i \varphi_i(\|x - x_i\|)$ which consists of N local functions centered $x_i, i = 1, \dots, N$. It can quickly learn the local features of arbitrary nonlinear function through several weighted RBFs close to the local sample. Figure 6 shows the fitting process of RBF interpolation.

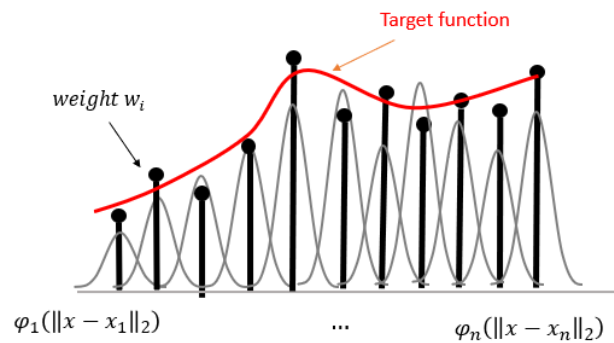


Figure 6. The fitting process of RBF interpolation.

5.3. Yield Estimation

The Figure of Merit (FOM) ρ defined in [4] is used to measure the convergence of different algorithm:

$$\rho = \frac{\sqrt{VAR_{\hat{p}_{fail}}}}{\hat{p}_{fail}} \quad (13)$$

where the $VAR_{\hat{p}_{fail}}$ is the variance of \hat{p}_{fail} . And $\rho < \varepsilon \sqrt{\log(1/\delta)}$ means one estimation has reached $(1 - \varepsilon)100\%$ accuracy with $(1 - \delta)100\%$ confidence. In this work, we set $\rho = 0.1$ which means 90% accuracy with 90% confidence level.

For generalization, the failure rate is expressed as equivalent sigma points of a standard normal distribution,

$$Y_\sigma = \phi^{-1}(1 - P(y > T)) \quad (14)$$

where ϕ is the standard normal cumulative distribution function (CDF) and T is the design specification which is our specified read access time threshold. $Y_\sigma = 3$ implies a cumulative probability of 0.99865 and a failure probability of 0.00135.

We compare the accuracy and efficiency of our model with other methods. Figure 7 illustrates the converged failure rate estimated by different methods. The golden reference is 4.12 sigma obtained by NanoYield, a state-of-art commercial yield analysis tool, which cost 4.6 hours. And the SSAIS shows 8.0% relative error with 3.79 sigma due to the weight degeneration in high dimension. HDIS shows 6.3% relative error with 4.38 sigma, which is better than SSAIS for its bounded conditional failure rate calculation to relieve the weight degeneration. Our model shows the relative of 0.7% with 4.15 sigma.

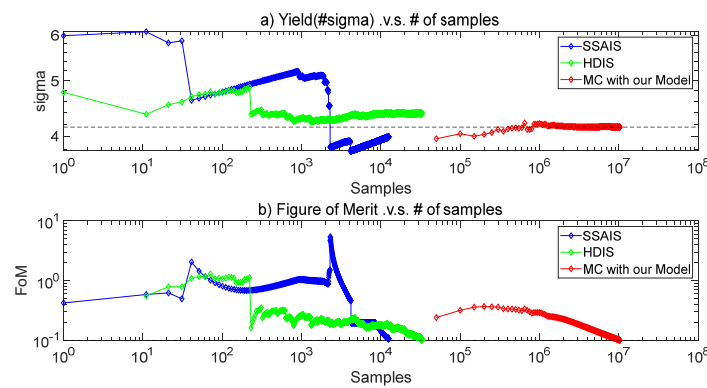


Figure 7. Yield convergence of different methods on 0.6V.

Both SSAIS and HDIS are sequence algorithms and the time cost is still large with 6 hours and 12 hours, respectively. Our model is trained with 600 samples. In additional, 3000 samples are used to search failure boundary. However, these samples are simulated parallel on the 10-cores server. And MC samples predicted by our model can be negligible compared simulation cost. Hence, the total cost of yield estimation based on our model is just 3.3 hours, which gains 1.4X speedup over NanoYield and 1.8X speedup over AIS and 3.6X speedup over HDIS. We also validate the yield prediction on different voltages. As shown in Figure 8, the maximum error of our model is 0.03 sigma and the average error is 0.01 sigma, which is very close to the golden reference.

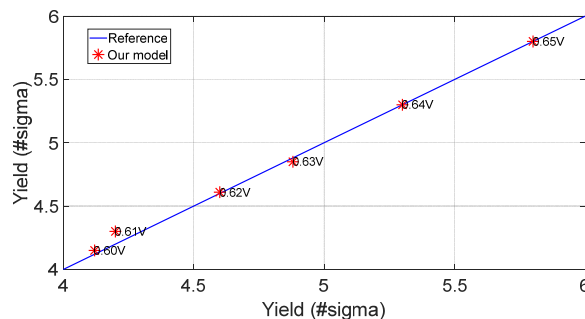


Figure 8. Yield estimation on different voltages.

6. Conclusions

In this paper, we proposed a sparse interactive meta model to accelerate the post-layout yield analysis. The model center is moved to the failure boundary by scaled shape sampling. And the model is derived in the group form, which can be solved very efficiently by our blockwise greedy

algorithm. The experimental results show that our method is 1.8X-3.6X faster than other state-of-art methods with little accuracy loss.

Author Contributions: Conceptualization, Xudong Zhang.; methodology, Xudong Zhang; validation, Lugang Li and Ruisi Yan.; formal analysis, Ruisi Yan.; writing—original draft preparation, Xudong Zhang.; writing—review and editing, Lugang Li.; supervision, Ruisi Yan.

Funding: “This research received no external funding”

Conflicts of Interest: The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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