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Article

Design of a Common-Mode Rejection Filter Using Dumbbell-Shaped Defected-Ground Structures Based on Equivalent Circuit Models

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Abstract: An efficient design method is proposed for a compact common-mode rejection (CMR) filter utilizing dumbbell-shaped defected ground (DS-DG) structures and gap-coupled stub (GCS) resonators. A CMR filter for differential lines helps to improve the signal integrity of high-speed digital signals on printed circuit boards. The proposed CMR filter design is based on the equivalent circuit models, while the previous designs depended heavily on the DS-DG structure optimization using the EM simulations. The proposed CMR filter effectively reject the common-mode components with minimally affecting the differential signals. To prove the simplified design approach, a 5th-order Chebyshev band-stop filter has been designed with three DS-DG structures and two GCS resonators. From the simulated and measured results, it is found that the proposed CMR filter provides ~90% fractional frequency bandwidth with more than 20 dB of common-mode rejection ratio and less than 0.6 dB of insertion loss of the differential signal.

Keywords: defected ground structure; dumbbell-shaped; Chebyshev band-stop filter; common mode rejection filter; gap-coupled stub; electromagnetic interference; signal integrity

1. Introduction

As the technology of 5G communications matures, preparing for 6G communications, the required speed of digital data transmission is ever increasing. A differential line (DL) is typically used to transmit high-speed digital signals on PCBs due to its advantages in the presence of external noise and electromagnetic (EM) interference [1]. In practical digital circuit boards, however, common-mode signal components can be generated with DLs due to the length difference between the two DL signal lines and unbalanced EM interference [1]. The common-mode signals have a strong tendency to radiate over other areas of the PCB, causing EM interference and deteriorating digital signal integrity [2]. In this case, in order to improve the signal integrity, a good-performing common-mode rejection (CMR) filter, blocking only common-mode components at the same time with minimal effect on differential-mode components, is required. For an ideal DL, when a differential-mode signal propagates, a virtual ground exists between the two lines, and the characteristic impedance is determined only by the interaction between the two lines of the DL, while that of a common-mode signal is determined only by fields between the individual signal line and the ground plane [1]. Therefore, in this ideal case, defected ground (DG) structures only affect the common-mode components without affecting the differential signals. With practical DLs on PCBs, however, differential signals can be affected by the DG structures as well, and numerous researches have been performed to find the optimum DG structures for CMR filters.

In order to implement good-performing CMR filters, various types of DG structures have been developed, such as Complementary Split Ring Resonator (CSRR), UH-shaped structure, Dumbbell-Shaped DG (DS-DG) structure, etc. [3-7]. The CSRR-type DG structure could provide more than 20

2

dB of common-mode rejection, but was difficult to design due to its complex equivalent circuit model, and did not provide a relatively broad operation bandwidth (1-1.7 GHz, 51.8%) [4]. A double-slit CSRR with a slot (S-DBCSRR) structure was proposed to provide 91% of the CMR bandwidth, but had more than 3 dB insertion loss of the differential signals for the operation bandwidth [5]. A UH-shaped DG structure was developed to operate for wideband frequencies from 3.6 to 9.1 GHz (86.6%), but it had a relatively low CMR ratio (< 20 dB) and a high insertion loss of differential signals (max. 4 dB) [6]. On the other hand, DS-DG structures could provide more than 20 dB of common-mode rejection with low insertion loss of the differential signals. DS-DG structures, however, had slow rejection skirt, and the conventional design method heavily depended on time-consuming optimization of EM simulations [7].

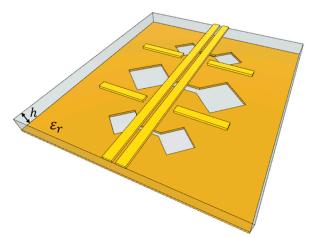
To date, numerous researches have been performed to improve the performance and design method of the DS-DG-based CMR filters: slow rejection skirt and design dependency on heavy EM simulations. In order to improve the rejection skirt property, a 3rd-order Chebyshev filter with three DS-DG structures was implemented [8]. With this type of the CMR filter, however, to achieve the higher-order Chebyshev filter, more DS-DG structures were required, increasing the structure size. On the other hand, a research to improve the design method was performed [9]: i.e., S-parameters of the DS-DG structure were first obtained with EM simulations, and then the impedance matrix was derived to calculate the equivalent inductances and capacitances. In another research, the capacitance of a DS-DG structure was approximated as the slot-line capacitance of the DS-DG structure, and then the inductance values were obtained using the Time-Domain Reflectometry (TDR) [7]. These design methods, however, still depended heavily on EM simulations. To obtain different values of equivalent capacitance and inductance for the Chebyshev or maximally-flat filters, the EM simulation should be performed again for each circuit value.

In this paper, an efficient design method of a DS-DG CMR filter, based on the equivalent circuit models without depending on EM simulations, is proposed. Also, the implemented filter is compact in size. Three DS-DG structures with two gap-coupled stub (GCS) resonators are used to achieve a 5th-order Chebyshev filter, which has the equivalent size of a 3rd-order Chebyshev filter with the convectional designs. The implemented CMR filter provides good performance with more than 20 dB of rejection and about 90% frequency bandwidth.

2. Structure and Analysis

2.1. Configuration of a DS-DG CMR filter

A perspective view of the proposed DS-DG CMR filter is shown in Figure 1. On top of the substrate, a DL is laid out alongside with two pairs of gap-coupled stub (GCS) resonators placed between the DS-DG structures. On the bottom, three DS-DG structures with two different sizes of dumbbell-shaped apertures are laid out. The CMR filter is implemented with the Rogers RO4003C substrate ($\varepsilon_r = 3.38$, $\tan\delta = 0.0027$, h = 0.508 mm). The characteristic line impedance of the DL is 100 Ω , and the size of the CMR filter is 21.9×18.7 mm².



Simplified equivalent circuits for the DS-DG structure and the GCS resonator are shown in Figure 2 [10,11]. The DS-DG structure can be represented by a parallel *LC*-resonator series-connected on the line, and the GCS resonator can be expressed by a series *LC*-resonator shunt-connected from the line.

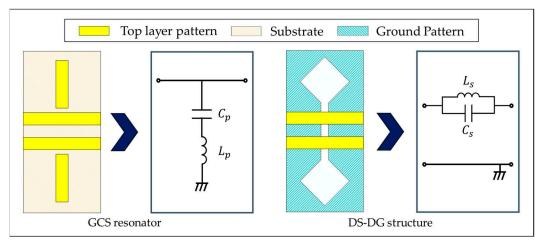


Figure 2. Equivalent circuit models of a GCS resonator and a DS-DG structure.

For the proposed CMR filter, in order to obtain high rejection and selectivity, a 5th-order Chebyshev band-stop filter is designed, and the element values of the low-pass prototype filter with 0.5 dB ripple are listed in Table 1.

Table 1. Element values of a 5th-order Chebyshev low-pass filter prototype with 0.5 dB ripple.

N	${m g}_{m 1}$	$oldsymbol{g_2}$	$oldsymbol{g}_3$	${m g}_4$	${m g}_5$
5	1.7058	1.2296	2.5408	1.2296	1.7058

The values of L_s , C_s , L_p , and C_p of the CMR filter can be calculated from g_k using (1) and (2) with the required bandwidth (B_w) and center frequency $(\omega_0 = 2\pi f_0)$.

$$C_s = \frac{1}{B_w g_k \times Z_0}$$
, $L_s = \frac{B_w g_k \times Z_0}{\omega_0^2}$ (1)

$$C_p = \frac{B_w \ g_k}{\omega_0^2 \times Z_0} \ , \qquad L_p = \frac{Z_0}{B_w \ g_k}$$
 (2)

With the proposed CMR filter, the center frequency is selected as 4.5 GHz with 4 GHz bandwidth (2.5 to 6.5 GHz). Figure 3 depicts an equivalent circuit for the 5th-order Chebyshev band-stop filter to reject the common-mode components. The filter response with the filter parameters using ideal lumped elements is shown in Figure 4. The center frequency is about 4.85 GHz, and the 3 dB-bandwidth is 4.1 GHz (2.8 to 6.9 GHz). Also, the bandwidth of 20 dB rejection is 3.2 GHz (3 to 6.2 GHz). There are two *S*₁₁ poles at 2.05 and 2.75 GHz, verifying a 5th-order filter response.

Figure 3. Equivalent circuit for the 5th-oder Chebyshev band-stop filter to reject the common-mode components.

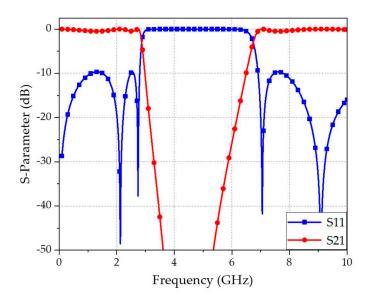


Figure 4. Chebyshev band-stop filter response with the proposed filter parameters.

2.2. DS-DG structure and its equivalent circuit model

A DS-DG structure used in the proposed CMR filter is illustrated with design parameters in Figure 5. The dumbbell-shaped aperture is formed symmetrically across the DL on the bottom of the substrate. The diamond-shaped heads are connected with a dumbbell handle as a slot line. As a signal flows through the DL, an induced surface current flows along the periphery of the dumbbell-shaped aperture, equivalently represented by the corresponding inductance and capacitance.

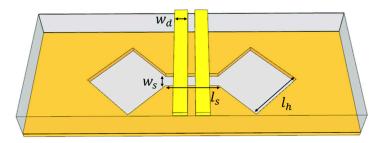


Figure 5. Dumbbell-shaped defected-ground (DS-DG) structure with design parameters.

If a common-mode signal propagates along a DL and is encountered with a DS-DG structure, surface currents on the bottom substrate, circulating around the sides of the dumbbell-heads, are formed. Each extra current path on the ground plane induces an inductance value in addition to the capacitance value induced by the current path along the slot-line (i.e., dumbbell-handle) of the DS-

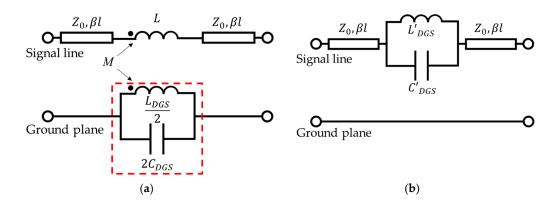


Figure 6. Equivalent circuit model for a DS-DG structure: (a) Initial equivalent circuit, (b) Modified equivalent circuit transformed with mutual magnetic coupling.

The C parameter value of the equivalent circuit in Figure 6a can be obtained by using (3) [7].

$$C_{DGS} = 2 \left[\frac{\varepsilon_0 \varepsilon_r}{\pi} \ln \left(\coth \left(\frac{\pi w_s}{4 \times h} \right) \right) + \frac{\varepsilon_0}{\pi} \ln \left(\frac{2 + 2\sqrt{q'}}{1 - \sqrt{q'}} \right) \right]$$
(3)

$$q = w_s/l_h$$
 , $q' = \sqrt{1 - q^2}$ (4)

where w_s is the width of the dumbbell-handle, and l_h is the length of a side of the dumbbell-head. Also, ε_r is the relative permittivity of a substrate. Eq. (3) approximates the capacitance of a slot-line of the DS-DG structure where q' in (3) is given as (4). In Eq. (3) the first term on the right-hand side represents the slot-line capacitance of the dumbbell-handle, and the second term is the capacitance of a dumbbell-head as an open slot-line.

In addition, the *L* parameter value of the equivalent circuit in Figure 6a can be obtained by using (5) [12].

$$L_{DGS} = \frac{\eta_0}{\sqrt{\mu_0 \varepsilon_0 \varepsilon_r}} \frac{l_p}{\frac{w_c}{h} + 1.393 + 0.667 \ln\left(\frac{w_c}{h} + 1.444\right)}$$
(5)

where η_0 is the intrinsic impedance of an EM wave in free space, and l_p is the total length of the surface current circulating around the periphery of a dumbbell head ($l_p = 4 \, l_h$). Also, h is the substrate thickness, and w_c is the width of surface current circulating around the periphery of a dumbbell-head. Eq. (5) estimates the inductance of the circulating surface current around the sides of a dumbbell. For simplicity of the design process, the surface current width (w_c) is approximated as the width (w_d) of one signal line of the DL. With an EM simulation, as shown in Figure 7, the current width of w_d corresponds to that of current density magnitude over 20 A/m. Also, it is found that a 10% change of w_c in (5) produces about a 2% change in resonance frequency, and the assumption of the surface current width as $w_c \cong w_d$ consistently results in good agreement with the EM simulated results.

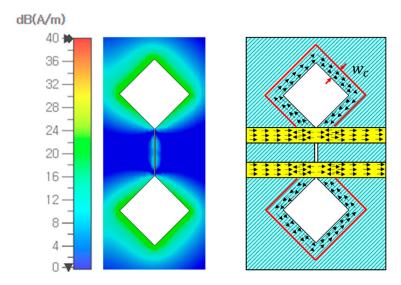


Figure 7. Surface current flow around the dumbbell-heads. The width of the surface current path (w_c) is approximated as that of the DL (w_d) .

In order to transform the parallel LC-resonator formed on the ground line to the one on the top signal line, as shown in Figure 6b, the mutual magnetic coupling coefficient can be used: i.e., $M = k_m \sqrt{L \cdot L_{DGS}}$ assuming $k_m = 1$ [13]. Therefore, the final L and C values of the equivalent circuit in Figure 6b can be expressed as (6) and (7) [10,13].

$$C'_{DGS} = \frac{L_{DGS}^2 C_{DGS}}{M^2} \tag{6}$$

$$L'_{DGS} = \frac{M^2}{L_{DGS}} \tag{7}$$

In order to prove the validity of assuming the surface current width as the width of the DL $(w_c \cong w_d)$ along with other equivalent parameter values for the DS-DG structure, various DS-DG structures have been tested. For example, the transmission-zero frequency is designed as 4.5 GHz for various DS-DG structures, and the transmission-zero frequencies between the calculated (with the approximated formulas) and EM-simulated values (using the CST Microwave Studio) are compared in Table 2. These resonant frequency values agree well within a 4.5% error, validating the above approximations.

Table 2. Comparison of the transmission zero frequencies between the calculation using the equivalent circuits and the 3D EM simulation.

Dumbbell-head shape	Square'	Triangula	rCircular	Diamond
Simulated (GHz)	4.68	4.79	4.5	4.84
Calculated (GHz)	4.506	4.506	4.547	4.526
Dumbbell-head perimeter l_p (mm)	25.6	25.6	25.2	25.4
Error (%)	3.86	6.3	1.03	6.93

2.3. GCS resonator and its equivalent circuit model

Using only an array of DS-DG structures on the bottom of the substrate, the complete Chebyshev band-stop filter cannot be attained since the shunt connections of series *LC* resonators in between the DS-DG structures are missing. For example, a previous CMR filter used three DS-DG structures to implement a Chebyshev band-stop filter, and the obtained filter response was similar to that of a 3rd-order Chebyshev filter [8]. In this paper, the proposed CMR filter additionally utilizes two pairs of gap-coupled stub (GCS) resonators in between the DS-DG structures alongside of the DL, thus

7

Figure 8a illustrates a configuration of two pairs of GCS resonators alongside of a DL. For the common-mode signal components propagating on the line, a GCS resonator works as a series LC resonator shunt-connected to ground at each signal line of the DL. With the differential signals, the propagation property is dominantly determined by the linewidth and gap of the DL line, minimally affected by the surrounding structures such as the GCS resonator [1]. The separation distance between the GCS resonators is chosen as $\lambda_g/4$, and the length of the GCS resonator is $\lambda_g/4$ with a 50 Ω line impedance.

The equivalent capacitance and inductance of a GCS resonator are expressed in (8) and (9) [11, 14].

$$C_{Res} = \frac{Z_0}{\omega_0 Z_U^2} \frac{g_k \Omega_c B_w}{g_0 \omega_0} \tag{8}$$

$$L_{Res} = \frac{Z_U^2}{\omega_0 Z_0} \frac{g_0 \omega_0}{g_k \Omega_c B_w} \tag{9}$$

where Z_U is the characteristic line impedance of one line of the DL, g_0 is the element value of the low-pass prototype, and Ω_c is the normalized cutoff frequency of the low-pass prototype filter.

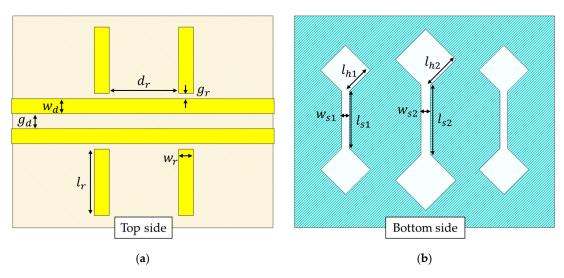


Figure 8. Configuration of the proposed CMR filter: (a) Top side with two pairs of GCS resonators, (b) Bottom side with three DS-DG structures.

3. Simulations and Measurements

The top and bottom sides of the proposed CMR filter with the DS-DG structures and GCS resonators are illustrated in Figure 8. The design parameters are listed in Table 3. Also, in Table 4, the L and C values of the proposed CMR filter for a 5th-order Chebyshev band-stop filter are compared between the theoretical and calculated values with equivalent circuit models for the proposed CMR filter structure. The equivalent L and C values of DS-DG structures are accurate within 1.6% error on average, and those of the GCS resonator are accurate within 3.83% error on average. Therefore, the L and C values obtained by the theoretical formulas and the calculated values using the equivalent models agree well.

Table 3. Design parameters of the proposed CMR filter.

Parameter	Dimension (mm)	Parameter	Dimension (mm)
w_d	0.96	l_{H1}	3.397
g_d	0.7874	l_{S1}	3.81

g_r	0.1	W_{S1}	0.1
d_r	7.62	l_{H2}	5.08
l_r	7.366	l_{S2}	4.7
W_r	1.2446	w_{S2}	0.3

Table 4. Comparison of the theoretical and calculated LC values.

Structu	re	Theoretical	Calculated	Error (%)
DC DCC home 1	L (nH)	2.87	2.9	1.04
DS-DGS type 1	C (pF)	0.455	0.45	1.1
DC DCC trung 2	L (nH)	4.28	4.31	0.7
DS-DGS type 2	C (pF)	0.306	0.317	3.59
CCC management and	L (nH)	1.58	1.64	3.8
GCS resonator	C (pF)	0.829	0.797	3.86

The proposed CMR filter with the DS-DG structures and GCS resonators is fabricated for measurements. Figure 9 shows the top and bottom views of the proposed CMR filter.

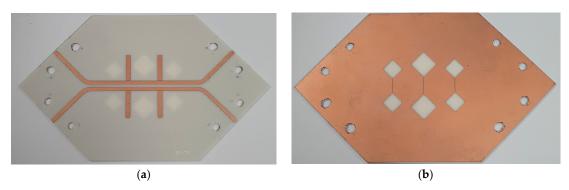


Figure 9. Pictures of the fabricated CMR filter with the DS-DG structures and GCS resonators: (a) Top side, (b) Bottom side.

The performance of the proposed CMR filter is measured with a four-port vector network analyzer (VNA: R&S ZNB40). As shown in Figure 9a, the two signal lines of the DL at the launch and receive ends spread apart to form two 50 Ω microstrip lines for connection to the 4-port VNA. The measured 4-port S-parameters are configured as mixed-mode S-parameters to monitor separately the differential-mode and common-mode signals. The simulated and measured performances of the proposed CMR filter are shown in Figure 10. The simulation is performed with a commercial 3D EM simulator (CST Microwave Studio). The simulated and measured S-parameter results agree very well. The implemented CMR filter provides the characteristics of a 5th-order Chebyshev band-stop filter. For the differential signals, the insertion loss (Sdd21) is less than 0.6 dB from DC to over 10 GHz. For the common-mode signal components, more than 20 dB rejection (Scc21) is obtained for the frequency range of 3 to 7.8 GHz, with transmission zero frequencies at 1.8 and 2.45 GHz. Therefore, the proposed CMR filter with the DS-DG structures and GCS resonators provides very effective rejection of common-mode components at the same time with minimal effect on the differential-mode signals.

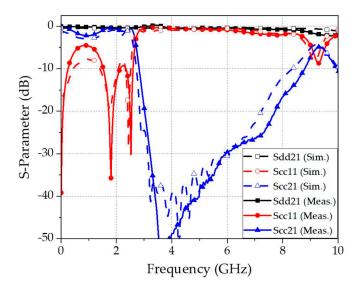


Figure 10. Simulated and measured S-parameters of the proposed CMR filter.

The performance comparison between the proposed CMR filter and the reported ones is given in Table 5. The maximum insertion loss means the maximum insertion loss of the differential signal, the differential signal bandwidth is obtained with that less than 3 dB insertion loss, and the rejection bandwidth is obtained with that more than 20 dB rejection level. As can be seen, as compared with the previous reported results, the proposed CMR filter provides very good performance with wide rejection bandwidth (~90%) for the common-mode signals and low insertion loss (max. 0.6 dB) for the differential signals.

Table 5. Performance comparison between the proposed and the reported CMR filters.

Reference Parameters	[4]	[5]	[6]	[7]	This work
Max. insertion loss (dB)	0.6	3	4	0.8	0.6
Diff. signal Bandwidth (GHz)	DC – over 2.5	5 DC – 3.9	DC - 8	DC - over 8	DC - 10
Daiastian Panduridth (CUa)	1 - 1.7	1.52 - 4.07	3.6 - 9.1	3.3 - 5.7	3 - 7.8
Rejection Bandwidth (GHz)	(51.8%)	(87.8%)	(75.9%)	(53.3%)	(88.9%)
Size (λ_g^2)	0.62×0.125	0.43×0.150	0.44×0.44	40.68×0.41	0.53×0.45

4. Conclusion

This paper proposes a simplified design approach for a compact and good-performing CMR filter with the DS-DG structures and GCS resonators. The proposed CMR filter design is based on equivalent circuit models for the DS-DG structures and GCS resonators. Previous design methods of DS-DG CMR filters heavily depended on tuning and optimization of the DG structures using 3D EM simulations. With this proposed CMR filter design, EM simulations are only used for performance verification. The proposed CMR filter structure is designed to effectively reject the common-mode signal components with minimal effect on the differential-mode signals. To validate this design approach, a 5th-order Chebyshev band-stop filter to reject the common-mode signals is designed, implemented, and measured. Simulated and measured performances agree very well. The implemented CMR filter provides the insertion loss (Sdd21) of less than 0.6 dB from DC to over 10 GHz for the differential signals, and more than 20 dB rejection for the frequency range of 3-7.8 GHz for the common-mode signal components. The proposed compact, high-performing CMR filter can be applied to improve the signal integrity of high-speed digital signals on PCBs.

10

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