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Empirical Characterization of ReRAM devices using Memory Maps and Dynamic Route Map

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Abstract: Memristors were proposed in the early '70s of the XXth century by Leon Chua as a new electrical element linking the charge and the flux. Since that first introduction, these devices have positioned themselves to be considered as possibly fundamental for the new generations of electronic devices. It has to be mentioned that actual memristors have only been recognized to exist as physical elements. In this paper, we apply a modeling framework to generate a model describing experimental measurements performed on a ReRAM. We show how to apply the Dynamic Route Map technique in the general case to obtain an approximation to the differential equation that determines the behaviour of the device.

Keywords: Memristor; RRAM; compact modeling; Charge and Flux; Phase space

1. Introduction

At the beginning of the 1970s Leon Chua had noticed the possible symmetry between the current i, the voltage v, the charge q and the flux φ in electric theory.[1]. The three known passive elements are capacitor, inductor, and resistor. Each element relates a pair of electrical variables: charge with voltage, flux with current, and current with voltage, respectively. Chua suggested the existence of a new fourth electrical element to find the missing elation between charge and flux. This proposed element would behave as a resistor with a memory, thus the name 'memristor'.

In fact, the first experimental description of such a kind of elements had already been performed long time ago [2], but didn't get into the circuit theory mainstream. The theoretical introduction of these devices paved the way to develop a generalization description a class of devices and systems (memristive systems or devices), that are intrinsically nonlinear and defined by a set of differential equations relating the electrical magnitudes to a set of internal state variables [3].

Currently, these novel devices are considered as one of the most promising elements for the next generation of integrated circuit (IC), beyond and more than Moore [4]. For instance, these devices are expected to be a path for alleviating the classical problem of the Von Neumman architecture caused by the need of transferring data between memories and processors, enabling in-memory computing [5]. Thus, an increasing number of applications using memristors are been proposed: innovative memories (ReRAMs, MRAM, etc.) [6–8], new sensor devices [9–12], elemental blocks for artificial neural networks (ANNs) and other bio-inspired systems [13–15], to cite just a few.

Related to fabrication technologies, memristive devices are currently being implemented in many different flavors, ranging from spintronics [16] to many different oxides [17–20], organic materials [21,22], or even emulators [23? –26]. However, it has to be noted that, due to the relatively immature technology, only a handful of foundries are providing memristors into their libraries as standard blocks, as is usual with resistors or



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capacitors. For instance, and as far as we know, RRAMs are offered by TSMC, Intel and Towerjazz (where this last one seems to be in process of being acquired by Intel), but using old nodes. This implies that they can be implemented in cheaper designs, allowing their use in low-cost edge IoT platforms [27]. The low maturity also leads to the same problems that were found in other technologies, as the reliability or the various forms of variability they present: the usual device-to-device, plus a cycle to cycle variability [28,29].

The simulation of these devices is also a hot topic inside the modelling community [30,31], and there are two main approaches to this task. The first approach uses current and voltage [32–36] as the electrical variables of interest. The second approach, in line with Chua's original proposal, uses instead charge and flux approach [37????-41].

An interesting way to check a model is using a tool named as the Dynamic Route Map (DRM). This technique helps to represent how the system's governing variable evolves versus its rate of change (the temporal derivative). As is discussed in the corresponding section, this representation can provide a very significant insight on how these variables evolve and their relations. This DRM can also be understood inside the formalism proposed by Corinto [42], as the equation relating the internal variables with their derivatives. Thus, in this paper, we will exploit this relation, and we will use experimental measurements to get an approximation to this relation that will allow us to model the behavior of the device.

2. Memristor Modelling Framework

As hinted in the introduction, a framework for memristor modelling was proposed by Corinto and Chua in [42]. In that work, the authors propose a consolidated theoretical framework that allows a unified approach to modelling, and they also check the possible advantages of the flux–charge $(\varphi$ -q) variables used in this context. In this proposal, they define the different memristors presented in the taxonomy by [43] (the ideal, the generic, and the extended memristor) as being just different approximations in the equations. The most general type in this classification are extended memristors, which are memristors that are described by additional state variables $\mathbf{X} = (X_1, X_2, ...)$ (other than just the electrical φ and q, or v and i). The most general expression relating all these variables can be expressed as follows

$$Q = f(\varphi, v, \mathbf{X}) \tag{1}$$

where the flux (φ) and the charge (Q) are linked to the voltage (v) and the current (i) respectively as follows

$$\frac{d\varphi}{dt} = v \tag{2}$$

$$\frac{dQ}{dt} = i \tag{3}$$

In addition, the evolution of the internal variables **X**, is described by a differential equation:

$$\frac{d\mathbf{X}}{dt} = \mathbf{g}_{\prime}(\varphi, v, \mathbf{X}) \tag{4}$$

In order to obtain an equivalent expression to Ohm's law, Eq. 1 can be derived with respect to time:

$$i = \frac{dQ}{dt} = \frac{\partial f(\varphi, v, \mathbf{X})}{\partial \varphi} \frac{d\varphi}{dt} + \frac{\partial f(\varphi, v, \mathbf{X})}{\partial v} \frac{dv}{dt} + \sum_{i} \frac{\partial f(\varphi, v, \mathbf{X})}{\partial X_{i}} \frac{dX_{j}}{dt}$$
 (5)

The above equation can be further simplified using the Lagrangian L and the Jacobian J, as expressed in Eqs. (6) and (7), respectively:

$$L(\varphi, v, \mathbf{x}) = \frac{\partial g_{\varphi}(\varphi, v, \mathbf{x})}{\partial v} \tag{6}$$

$$\mathbf{J}(\varphi, v, \mathbf{X}) = \left(\frac{\partial g_{\varphi}(\varphi, v, \mathbf{X})}{\partial X_1}, ..., \frac{\partial g_{\varphi}(\varphi, v, \mathbf{X})}{\partial X_n}\right) \tag{7}$$

$$i = \frac{\partial f(\varphi, v, \mathbf{X})}{\partial \varphi} v + L(\varphi, v, \mathbf{x}) \frac{dv}{dt} + \mathbf{J}(\varphi, v, \mathbf{x}) \cdot \frac{d\mathbf{X}}{dt}$$
(8)

It is interesting to point out that the two last right terms in Eq. 8 have a clear physical meaning, being parasitic elements. Especially, the part with dv/dt can be connected to a capacitive element, while the term with dX_j/dt can be identified as a current source. In the case where parasitic elements are present, the most general (i.e. extended) memristors may be reduced to generic memristors (plainly, just memristors). This is caused by function g_{φ} being dependent only on flux φ and the state variables \mathbf{x} , thus L=0. Moreover, the original memristors, as defined in [1] are generic memristors that don't depend on any internal state variable (i.e. J=0). It has also to be noticed that it has been demonstrated in [42] that the following relation has to hold:

$$L(\varphi, v, \mathbf{x}) \frac{dv}{dt} + \mathbf{J}(\varphi, v, \mathbf{x}) \cdot \frac{d\mathbf{X}}{dt} = 0$$
(9)

Therefore, we can determine the memristance M to be as follows

$$M(\varphi, v, \mathbf{X}) = \frac{\partial f(\varphi, v, \mathbf{X})}{\partial \varphi} = f_M(\varphi, v, \mathbf{X})$$
(10)

The above expressions can also be formulated in terms of charge and current as of the variables as in [?]. In this case, the parasitic elements would have been inductors, and voltage sources [42].

In brief, the relevant relations for a flux-controlled memristor can be described using Eqs. 3, 4 and 11:

$$v = M(Q, i, \mathbf{X}) \cdot i \tag{11}$$

The above Eq. (11) shows the dependence of the memristance in the most general case (i.e. an extended memristor). There, the voltage across the terminals is named v, while i is the current and Q is the charge (the first momentum of the current, as in [42]). The internal variables are included as the vector \mathbf{x} , and their dynamics are defined by $\mathbf{g}_{\mathbf{Q}}$ and Eq. 4.

It is worth to point out that the overhead framework has already been employed to successfully model various sorts of memristive systems, additionally enhancing the generalized framework for compact modelling in the flux-charge space [44]. Some interesting works utilising this approach may be found in the literature, as [45] where a memristor model is developed using a charge-dependent mobility. A model for PCM is proposed in [37], and in [38] or [40], the authors proposed semi-empirical models for ReRAMs, being considered to be memristors, and [46?] where a Monte Carlo model for ReRAMs was presented. A delay model for memristive crossbars was derived in [47] utilizing a flux-charge definition. Finally, in [48] light bulbs based on incandescent filaments are shown to be generic memristors, while [49,50] perform a experimental characterization of a memristive system by using the flux-charge notation, and present a discussion of the influence of the waveform's frequency and shape.

3. The Dynamic Route Map

It is common knowledge that Nonlinear Dynamics are illustrated and further studied by utilizing the state space (otherwise known as the phase space) approach. This approach was initially introduced by J. Liouville [51], further utilized by H. Poincaré to study the three-body problem [52]. Since then, the study of nonlinear systems and the relevant methods in nonlinear dynamics have been developed within the phase space approach. This was due to the fact that important features and attributed of nonlinear systems are clearly emerging within the state space. It should be noted that the case of the two-dimensional phase space, also mentioned as phase portrait (($x = t^2 + t^2 +$

On the other hand, the study of memristors is conducted within the previously described framework (Section 2), where the dynamics of an extended memristor that is flux-controlled is presented. It is apparent that due to the principle of duality, the corresponding charge-controlled extended memristor could be easily described within the same frame. Clearly, memristors are nonlinear elements and that is due to their hysteretic behaviour, which is their main fingerprint [54]. As a result, phase space emerges as the proper space for studying memristor dynamics and this way useful information on their dynamical properties could be provided. Toward this, a new tool in the phase space, the recently introduced, novel technique of the Dynamic Route Map has been proposed for studying memristor devices. This technique allows for acquiring information regarding their switching properties and features in general [55].

The dynamic route concept within the phase space as this was applied in the case of memristors, considers the definition of the trajectory of one of the system's state variables as the voltage or the current through the device (according on whether the memristor is flux or charge controlled) gets a specific value. This procedure can be considered to be equivalent to plotting equation 4. Expanding this concept from a specific trajectory to a family of trajectories, the Dynamic Route Map is defined as a a parametric collection of a theoretically infinite number of dynamic routes [55]. An illustration of a typical DRM is presented in Figure 1, where three representative cases of DRM are presented. The one in the middle is a characteristic case that includes one stable and two unstable equilibria. It is apparent that the DRM is infinitely dense and that for properly visualizing it usually only a finite number of them is presented.

A comprehensive apposition of the features demonstrated by the DRM technique appears in [55]). For the reader's convenience, we present the most important in the lines that follow:

- Any point belonging to the positive half-plane,i.e., dx/dt > 0 moves to the right of its dynamic route, thus increasing the value of variable x.
- any point belonging to the negative half-plane,i.e., dx/dt < 0 moves to the left of its dynamic route, thus decreasing the value of variable x.
- The higher a dynamic route is located, the faster a point will move along it, as long as this route belongs to the upper half-plane (thus its points move to the right).
- The lower a dynamic route is, the faster a point will travel along it, as long as this route belongs to the lower half-plane (thus its points move to the left).
- All the points with null velocity dx/dt = 0),i.e., those found on the horizontal axis, define equilibrium states of the system, and are called equilibrium points.
- Equilibrium points may be stable or unstable. This means that the dynamic routes that lead to them may converge towards them or diverge from them, correspondingly.

It is very important to mention that the dynamic route corresponding to a null parameter value (i.e. for memristors that would be a voltage v=0 or a current i=0) defines the so-called Power-Off Plot (POP), which provides the stability points [?]. In specific, memristors having one stable and two unstable equilibria appear to be volatile, while memristors with one unstable and two stable equilibrium points appear to be non-volatile.

As a final point, we would like to mention that the DRM can be used to study the dynamics of the switching process in a memristive device (i.e. the setting and resetting

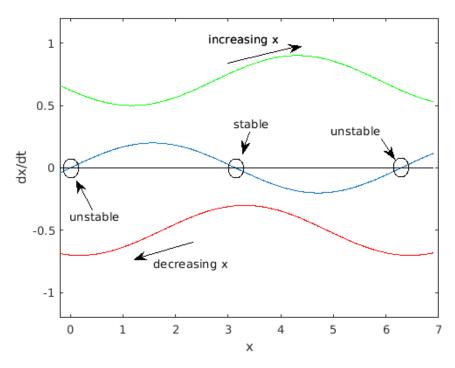


Figure 1. Example of an arbitrary typical DRM. The three curves correspond to three different situations: the blue line presents three intersection points with zero, thus showing that the system, for that case, has two unstable and a stable points. The red line shows a case where the variable x is always decreasing, while the green line shows the opposite case where x is always growing.

procedures). These dynamics can be visualized on the DRM, since these two operations are achieved when the operation point is obliged to shift its dynamic route, during some time; something that happens when positive or negative voltage pulses are applied, further resulting in switching between two equilibria.

4. Experimental Results

4.1. Description of the used devices

The devices used are $TiN/Ti/HfO_2/W$ metal–insulator–metal (MIM) stacks [56]. They were made on a 100 mm-diameter Si-n⁺⁺ wafer by doing first a deposition of a 200 nm-thick Ti adherence layer, which was then followed by a deposition of a 50 nm-thick W layer. Both of those layers were grown using Magnetron sputtering. A plasma-enhanced chemical vapor deposition (PECVD) using silane (SiH₄) as the precursor process was used to create a 100 nm SiO₂ isolation oxide. This was in turn patterned using photolithography and dry etching. The apertures of the SiO₂ layer define the active area of the MIM devices. After that, the 10 nm HfO₂ film was deposited by atomic layer deposition (ALD) at 225 °C using Tetrakis(dimethylamino)hafnium (TDMAH) and H₂O as precursors and N₂ as the carrier and purge gas. Magnetron sputtering was used again to define the top electrode as a metal layer of 10 nm-thick Ti and a 200 nm-thick TiN. The electrode was patterned by a lift-off process.

During the last step, magnetron sputtering was used to deposite a 500 nm Al layer on the back of the wafer for electrically contacting the botton electrode through the Si-n⁺⁺ substrate used to fabricate the devices. The final TiN/Ti/HfO₂/W devices present a square shape, and the fabricated areas range between 22 um² and 120120 um²

A typical curve representing the evolution of the conductance *G* for a set of different amplitudes of a triangular input waveform is presented in Fig. 2.

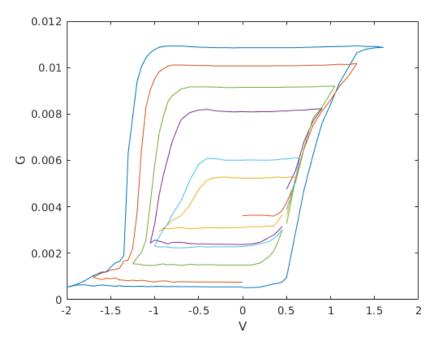


Figure 2. Measured values of the conductance G (in S), for different amplitudes of the applied input waveform.

4.2. Model Generation

The previous sections describe the theoretical framework. In this section, following the work published in [?], we propose using a experimental approximation to the variation of the memristance:

$$i = \frac{1}{M} v = G v \tag{12}$$

where the dynamics of the memconductance *G* is determined by a differential equation:

$$\frac{dG}{dt} = g_G(G, v) \tag{13}$$

It is clear that Eq. 13 corresponds to a possible DRM. Due to our election of G as the variable of interest, it is possible to calculate an approximation for dG/dt from the experimental data, plotting it as a function of G and v.

As a first step, we have approximated the temporal variation of *G* by a discrete approximation:

$$\frac{dG}{dt} \approx \frac{G(t) - G(t - dt)}{dt} \tag{14}$$

where dt is the time interval between two samples. Once this is calculated, we can plot the DRM for this data as a 3D picture, as shown in Fig. 3. This Figure shows a subset of the data, marked as the lines with a 'x' symbol. This subset of data has been then used to obtain an approximation to Eq. 13 with a polynomial function (using MATLAB *polyfit55*).

$$g_G(G, v) \approx \sum_{i=0..5, j=0..3} p_{i,j} v^i G^j$$
 (15)

The value of each coefficient $p_{i,j}$ is provided in Table 1, together with the 95% confidence

Table 1: Coefficients for the approximation of g_G in Eq. 15. The units for coefficient p_{ij} are $s^{-1}V^{-i}\Omega^{j-1}$.

Coefficient	Value	95% Confidence
p ₀₀	-3.19E-04	(-5.93E-04, -4.56E-05)
p_{10}	3.77E-04	(-1.03E-04, 8.57E-04)
p ₀₁	3.42E-01	(-7.88E-02, 7.63E-01)
p ₂₀	1.96E-03	(1.04E-03, 2.88E-03)
p ₁₁	-4.70E-01	(-9.22E-01, -1.84E-02)
p ₀₂	-1.24E+02	(-3.43E+02, 9.56E+01)
p ₃₀	3.52E-03	(2.18E-03, 4.87E-03)
p ₂₁	2.68E-01	(-2.57E-01, 7.93E-01)
p ₁₂	2.28E+02	(8.40E+01, 3.72E+02)
p ₀₃	1.33E+04	(-3.44E+04, 6.10E+04)
p_{40}	2.26E-03	(1.37E-03, 3.14E-03)
p ₃₁	-1.41E-01	(-5.16E-01, 2.34E-01)
p ₂₂	-1.62E+02	(-2.46E+02, -7.74E+01)
p ₁₃	-3.65E+04	(-5.37E+04, -1.93E+04)
p_{04}	-4.13E+04	(-4.57E+06, 4.49E+06)
p ₅₀	4.40E-04	(2.31E-04, 6.48E-04)
p ₄₁	-2.46E-01	(-3.50E-01, -1.42E-01)
p ₃₂	-2.41E+01	(-4.71E+01, -9.93E-01)
p ₂₃	1.15E+04	(7.47E+03, 1.55E+04)
p_{14}	1.80E+06	(1.11E+06, 2.49E+06)
p ₀₅	-3.56E+07	(-1.92E+08, 1.20E+08)

interval, as provided by MATLAB. Notice that this polynomial is actually an approximation to g_G . Figure 4 shows the surface defined by this approximation and the original data.

The validity of the proposed method has been tested using the fitted polynomial approximation to predict the response of the system to a specific input. This has been done by integrating the differential equation system formed by Equation 12 and 13, where g_M is the fitted approximation using the coefficients from Table 1 and the initial condition was chosen as the experimental initial value of G. The results of this integration are depicted in Fig. 5, where the thick lines are the model and the thin lines featuring symbols correspond to experimental data. The coincidence between the real data and the predicted by the model values is evident, and it could be improved by using a different approximation to the DRM, as for instance the so-called hysterons proposed in [57].

5. Discussion

As has been shown in the paper, the considered ReRAM devices can be modelled as ideal memristors using the formal framework proposed by Corinto et al [42] along with an experimental approximation to the differential equation governing the behavior of the memristance. This approximation has been calculated using a polynomial fitting to the experimental data. The results have been obtained by integration of this approximation plus the standard equations defined in Section 2. The comparison between the model and the experimental data show a fairly good agreement, justifying the used approach.

Further work related to this should be performed to check this conclusion. Specifically, the effect of the input signal velocity has to be tested, as well as the variability in the cycle to cycle switching characteristics.

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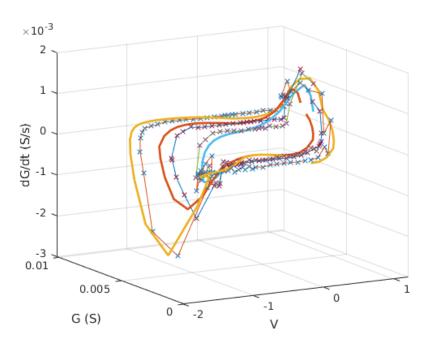


Figure 3. Experimental DRM, showing only a subset of the measured curves. Notice that this figure can be reduced to Fig. 2 if the dG/dt axis is ignored.

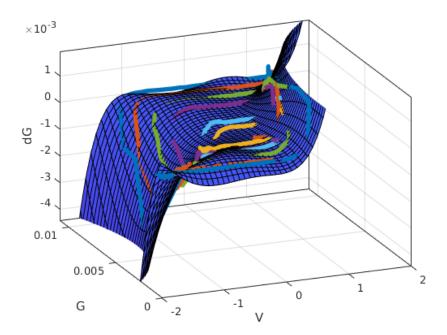


Figure 4. Surface fitting of the data in Fig. 3 using a polynomial.

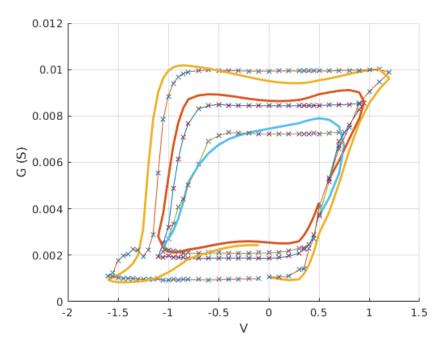


Figure 5. Results obtained using Eq. 15 with the coefficients in Table 1 into Eq. 13.

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