

Article

# Heterogeneous Distributed Big Data Clustering on Sparse Grids

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**Abstract:** Clustering is an important task in data mining that has become more challenging due to the ever-increasing size of available datasets. To cope with these big data scenarios, a high-performance clustering approach is required. Sparse grid clustering is a density-based clustering method that uses a sparse grid density estimation as its central building block. The underlying density estimation approach enables the detection of clusters with non-convex shapes and without a predetermined number of clusters. In this work, we introduce a new distributed and performance-portable variant of the sparse grid clustering algorithm that is suited for big data settings. Our compute kernels were implemented in OpenCL to enable portability across a wide range of architectures. For distributed environments, we added a manager-worker scheme that was implemented using MPI. In experiments on two supercomputers, Piz Daint and Hazel Hen, with up to 100 million data points in a 10-dimensional dataset, we show the performance and scalability of our approach. The dataset with 100 million data points was clustered in 1198 s using 128 nodes of Piz Daint. This translates to an overall performance of 352 TFLOPS. On the node-level, we provide results for two GPUs, Nvidia's Tesla P100 and the AMD FirePro W8100, and one processor-based platform that uses Intel Xeon E5-2680v3 processors. In these experiments, we achieved between 43% and 66% of the peak performance across all compute kernels and devices, demonstrating the performance portability of our approach.

**Keywords:** clustering; machine learning; distributed computing; performance portability; GPGPU; OpenCL; peak performance

## 1. Introduction

In data mining, cluster analysis partitions a dataset according to a given measure of similarity. The partitions obtained as a result of the clustering process are called clusters. The clustering of big datasets poses additional challenges as not all clustering algorithms scale well in the size of the dataset. Furthermore, mapping clustering approaches to modern hardware platforms such as graphics processing units (GPUs) requires new parallel approaches. And for the use on supercomputers or in the cloud, algorithms need to be designed for distributed computing.

There is a wide range of algorithms that perform clustering. The classic  $k$ -means algorithm iteratively improves an initial guess of cluster centers [1]. Efficient variants of the  $k$ -means algorithm have been proposed, e.g. by using domain partitioning through  $k$ - $d$ -trees [2] or by a more careful selection of the initial cluster centers [3]. As a major disadvantage,  $k$ -means requires the number of clusters to be known in advance, which is not always possible. Moreover, in contrast to many alternatives  $k$ -means cannot detect clusters with non-convex shape.

DBSCAN probably is the most widely-used density-based clustering approach [4]. In its basic form, it constructs a cluster based on the number of data points in an  $\epsilon$ -sphere around each data point. If spheres overlap and have enough data points in them, the data points are part of the same cluster. For  $m$  data points, the complexity of DBSCAN was stated as  $\mathcal{O}(m \log m)$  in the original paper [4]. However, more recent work shows that the actual complexity has a lower bound of  $\Omega(m^{4/3})$  [5,6].

38 DENCLUE is another example for clustering based on density estimation. It uses a kernel density  
39 estimation algorithm [7]. Spectral clustering methods cluster datasets by solving a mincut problem on  
40 a weighted neighborhood graph [8]. There are many more approaches to clustering, *e.g.* using neural  
41 networks [9], described in the literature [1,10].

42 Some clustering algorithms support GPUs for higher performance. Takizawa and Kobayashi  
43 presented a distributed and GPU-accelerated  $k$ -means implementation in 2006, before modern GPGPU  
44 frameworks like CUDA and OpenCL were available [11]. Since then, further GPU-enabled  $k$ -means  
45 algorithms have been developed [12–15]. Fewer published results are available for density-based  
46 GPU-accelerated clustering. CUDA-DClust is a GPU-accelerated variant of DBSCAN that uses an  
47 indexing approach to reduce distance calculations [16]. Andrade *et al.* developed a GPU-accelerated  
48 variant of DBSCAN called G-DBSCAN employing an algorithm with quadratic complexity in the  
49 dataset size [17].

50 While many clustering algorithms have been proposed, not many have been shown to work in  
51 big data scenarios.  $k$ -means++ is a map-reduce variant of the  $k$ -mean algorithm that has been used to  
52 cluster a 4.8 million data points dataset on a Hadoop cluster with 1968 nodes [18]. MR-DBSCAN is a  
53 DBSCAN variant that could cluster a 2d dataset with up to 1.9 billion data points and is implemented  
54 with a map-reduce approach as well [19]. The published results of MR-DBSCAN demonstrate excellent  
55 performance. However, it uses a grid discretization that makes assumptions on the distribution of  
56 the dataset throughout the domain. Furthermore, it is unclear how the algorithm will scale to higher  
57 dimensions, as the grid discretization is fully affected by the curse of dimensionality [20]. RP-DBSCAN  
58 implements a similar approach compared to MR-DBSCAN, but uses a more advanced partitioning  
59 scheme [5]. RP-DBSCAN was able cluster a 13d dataset with 4.4 billion data points.

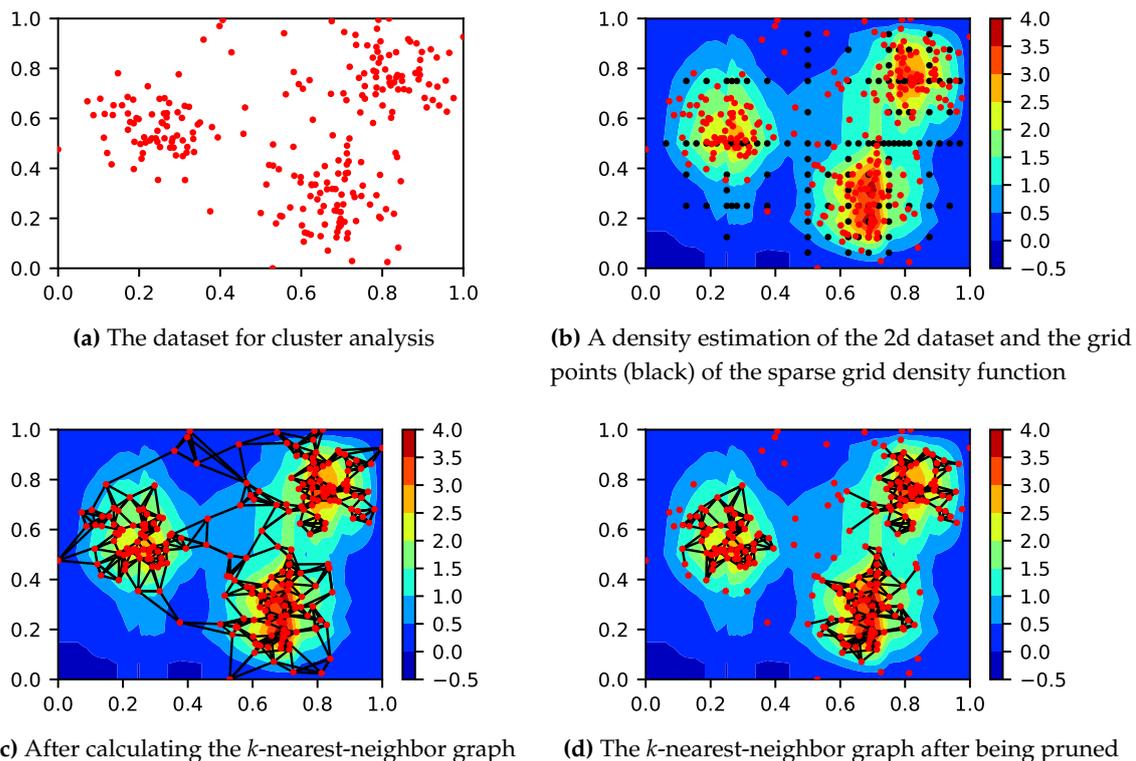
60 In this work, we introduce a new distributed and performance-portable variant of the sparse grid  
61 clustering algorithm. This approach builds upon prior work which presented the basic theory and  
62 compared our approach to other clustering strategies [21].

63 The unique building block of our approach is the sparse grid density estimation algorithm. Sparse  
64 grids are a method for spatial discretization that has been applied to higher-dimensional settings with  
65 up to 166 dimensions and moderate intrinsic dimensionality [22]. Therefore, in contrast to many  
66 spatial partitioning approaches, the underlying sparse grid density estimation mitigates the curse of  
67 dimensionality. The sparse grid clustering method does not rely on assumptions about the distribution  
68 of data, it can successfully suppress noise and it can detect clusters of non-convex shape. Compared to  
69  $k$ -means, sparse grid clustering does not require the number of clusters as a parameter of the algorithm.  
70 In this paper, we use the Euclidean norm as the measure for the similarity of data points.

71 Methods based on sparse grids have been used for regression and classification tasks [22,23].  
72 In prior work, we have shown the applicability of these methods in heterogeneous computing and  
73 high-performance computing settings [24–26]. However, to our knowledge this work presents the first  
74 high-performance results for sparse grid clustering.

75 Our algorithm was designed with a focus on high performance and performance portability. On  
76 the node-level we use OpenCL, as it offers basic portability across a wide range of hardware platforms.  
77 We not only support GPUs and processors of different vendors, our approach achieves a major fraction  
78 of the peak performance of all devices used. To map our method to clusters and supercomputers,  
79 we implemented a distributed manager-worker scheme. Due to the underlying method and the  
80 high-performance distributed approach, we show that sparse grid clustering is well-suited for large  
81 datasets. We provide results for 10d datasets with up to 100 million data points and 100 clusters.

82 The remainder of this paper is structured as follows. In Sec. 2, we give an overview of the  
83 sparse grid clustering algorithm. Then, in Sec. 3, we introduce the sparse grid density estimation  
84 as our core component. The other components of the algorithm are introduced in Sec. 4. Section 5  
85 describes the parallel and distributed implementation and discusses features of the algorithms from  
86 a high-performance perspective. We show results for three node-level hardware platforms and two



**Figure 1.** The application of the sparse grid clustering algorithm to a 2d dataset with three slightly overlapping clusters. After calculating the sparse grid density estimation and the  $k$ -nearest-neighbor graph, the graph is pruned using the density estimation. This splits the graph into three connected components.

supercomputers in Sec. 6. Finally, in Sec. 7, we remark on implications of the presented algorithm and discuss future work.

## 2. Clustering on Sparse Grids

In this section, we describe the sparse grid clustering algorithm on a high level. We describe its components in Sec. 3 and 4 in more detail.

Sparse grid clustering assumes a  $d$ -dimensional dataset  $T$  with  $m$  data points that was normalized to the unit hypercube  $[0, 1]^d$ :

$$T := \{\mathbf{x}_i \in [0, 1]^d\}_{i=1}^m. \quad (1)$$

We further assume that the dataset has been randomized.

The sparse grid clustering algorithm is a four step algorithm. Except for the last one, these steps are shown in Fig. 1 at the example of a 2d dataset with three clusters. The sparse grid clustering algorithm first calculates a density estimation of the dataset using the sparse grid density estimation algorithm (Fig. 1b). Then, a  $k$ -nearest-neighbor graph of the dataset is computed (Fig. 1c). In the third step, the density estimation is used to prune the graph. The algorithm prunes nodes in low-density regions and edges that intersect low-density regions (Fig. 1d). In the fourth and final step the weakly-connected components of the pruned graph are retrieved. The connected components of the graph are returned as the detected clusters.

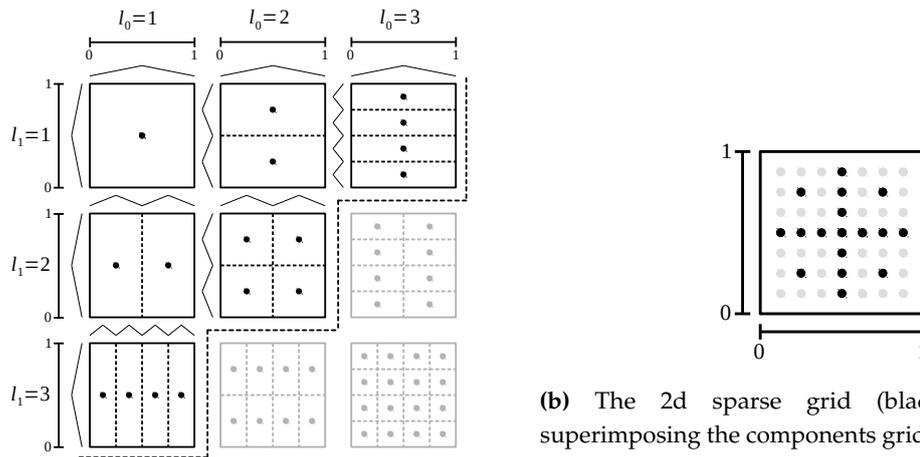
This description immediately suggests one of the tuning parameters of the algorithm. The sparse grid clustering method requires a carefully chosen threshold value  $t$  that is used for pruning the  $k$ -nearest-neighbor graph.



(a) A 1d full grid in nodal basis

(b) A 1d sparse grid in the hierarchical basis

**Figure 2.** The nodal and the sparse grid in Fig. 2a and Fig. 2b both have discretization level  $l = 3$  and are equal for  $d = 1$ . Both use hat functions  $\phi_{l,i}$  as basis functions, but in a nodal and in a hierarchical formulation. Note that sparse grids employ less grid points compared to full grids of the same level for  $d \geq 2$  (see Fig. 3).



(a) The subgrids of varying discretization level. Dotted lines outline the support of the basis functions centered at the grid points.

(b) The 2d sparse grid (black) obtained by superimposing the components grids.

**Figure 3.** The subgrids of a sparse grid with  $l = 3$  (Fig. 3a) and the resulting sparse grid (Fig. 3b). Greyed out subgrids and grid points would be part of the corresponding full grid.

### 104 3. Estimating Densities on Sparse Grids

105 The sparse grid density estimation is build upon the concept of sparse grids. We therefore briefly  
106 introduce sparse grids and then describe how densities can be estimated with the sparse grid method.

#### 107 3.1. Sparse Grids

108 As this work focuses on the sparse grid clustering algorithm and not on the basic sparse grid  
109 method, this introduction to sparse grids is necessarily brief. For a thorough presentation, we  
110 recommend the overview by Bungartz and Griebel [27].

111 A  $d$ -dimensional grid can be defined on the unit hypercube  $[0, 1]^d$  with an equidistant mesh width  
112  $h_n = 2^{-n}$  for a discretization level  $n$  and, therefore,  $2^{nd}$  grid points. With basis functions centered at  
113 the grid points, a corresponding function space is spanned by the linear combinations of the basis  
114 functions. We call this a full grid approach. Full grids can be represented in a hierarchical basis. From  
115 this representation, it is a small step to sparse grids.

The hierarchical approach constructs a final grid by superimposing a set of subgrids. First, we define an index set that is used to enumerate the grid points on the  $d$ -dimensional subgrids of discretization level  $\mathbf{l} \in \mathbb{N}^d$ :

$$I_{\mathbf{l}} := \{(i_1, \dots, i_d) : 0 < i_k < 2^{l_k}, i_k \text{ odd}\}. \quad (2)$$

In this work, we employ hat functions as basis functions. The scaled and translated 1d hat functions are defined as

$$\phi_{l,i}(x) := \max(0, 1 - |2^l x - i|). \quad (3)$$

For  $d > 1$ , we use a tensor-product approach:

$$\phi_{\mathbf{i},\mathbf{x}} := \prod_{j=1}^d \phi_{l_j,i_j}(x_j). \quad (4)$$

Given an index set  $I_1$  and the basis functions  $\phi_{\mathbf{i},\mathbf{x}}$ , we can define the subspaces

$$W_1 := \text{span}\{\phi_{\mathbf{i},\mathbf{x}} : \mathbf{i} \in I_1\}. \quad (5)$$

116 The subgrids and their grid points  $x_{\mathbf{i},\mathbf{x}} := (i_1 h_{l_1}, \dots, i_d h_{l_d})$  for the subspaces  $W_{(1,1)} \dots W_{(3,3)}$  are  
117 displayed in Fig. 3a for a 2d grid.

With the direct sum  $\oplus$ , a full grid of discretization level  $n \in \mathbb{N}$  in the hierarchical basis can be defined as

$$V_n := \bigoplus_{|\mathbf{i}|_\infty \leq n} W_1. \quad (6)$$

118 Figure 2 shows how a 1d grid is represented in the standard (nodal) basis (Fig. 2a) and the hierarchical  
119 basis (Fig. 2b).

Sparse grids are based on the observation that for sufficiently smooth functions only a small additional interpolation error is introduced if certain grid points are removed [27]. This mitigates the curse of dimensionality. As a result, the sparse grid function space  $V_n^{(1)}$  is constructed from a different set of subspaces:

$$V_n^{(1)} := \bigoplus_{|\mathbf{i}|_1 \leq n+d-1} W_1. \quad (7)$$

120 Figure 3 shows how a 2d sparse grid is constructed from subgrids that correspond to the subspaces of  
121 the grid.

A sparse grid function  $f \in V_n^{(1)}$  is given as

$$f(\mathbf{x}) = \sum_{|\mathbf{i}|_1 \leq n+d-1} \sum_{\mathbf{i} \in I_1} \alpha_{\mathbf{i},\mathbf{x}} \phi_{\mathbf{i},\mathbf{x}} =: \sum_{j=1}^N \alpha_j \phi_j(\mathbf{x}), \quad (8)$$

122 where we sum up all  $N$  weighted basis functions in some order, and where  $N$  denotes the total number  
123 of grid points. Since our algorithms iterate the basis functions linearly, we use the simplified notation  
124 when the algorithms are presented. The coefficients  $\alpha_{\mathbf{i},\mathbf{x}}$  are usually referred to as surpluses.

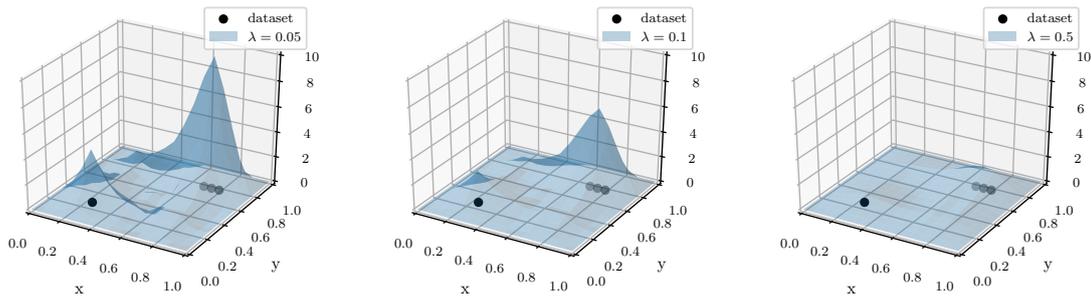
### 125 3.2. The Sparse Grid Density Estimation

The sparse grid density estimation, originally proposed by Hegland *et al.* [28], uses an initial density guess  $f_\epsilon$  than is smoothed using a spline-smoothing approach:

$$\hat{f} = \arg \min_{u \in V} \int_{\Omega} (u(\mathbf{x}) - f_\epsilon(\mathbf{x}))^2 d\mathbf{x} + \lambda \|Lu\|_{L_2}^2. \quad (9)$$

This approach results in a function  $\hat{f} \in V$  that balances closeness to the initial density guess with the regularization term  $\|Lu\|_{L_2}^2$  that enforces smoothness on the resulting density function. The regularization parameter  $\lambda$  controls the degree of smoothness of  $\hat{f}$ .  $L$  usually is some differential operator. We use the initial density guess proposed by Hegland *et al.* that places a Dirac delta function  $\delta_{\mathbf{x}_i}$  at every data point  $\mathbf{x}_i$ :

$$f_\epsilon := \frac{1}{m} \sum_{i=1}^m \delta_{\mathbf{x}_i}. \quad (10)$$



**Figure 4.** The effect of the regularization parameter  $\lambda$  on a 2d dataset with four data points. For smaller  $\lambda$  values, the function becomes more similar to the initial density guess of Dirac  $\delta$  functions. The function becomes smoother for higher values of  $\lambda$ .

As in prior work, we compute the best sparse grid function  $u \in V_n^{(1)}$  and use a surplus-based regularization approach [22]. Therefore, the problem to solve is

$$\hat{f} = \arg \min_{u \in V_n^{(1)}} \int_{\Omega} (u(\mathbf{x}) - f_{\epsilon}(\mathbf{x}))^2 d\mathbf{x} + \lambda \sum_{i=1}^N \alpha_i^2. \quad (11)$$

This formulation leads to a system of linear equations

$$(B + \lambda I)\mathbf{a} = \mathbf{b}, \quad (12)$$

126 with  $B_{ij} = (\phi_i, \phi_j)_{L_2}$ , the identity matrix  $I$  and  $b_i = \frac{1}{m} \sum_{j=1}^m \phi_i(\mathbf{x}_j)$ .

We solve this system of linear equations with a conjugate gradient solver (CG). Given this iterative solver, two major operations need to be performed: calculating the right-hand side once and computing the matrix-vector product  $\mathbf{v}' = (B + \lambda I)\mathbf{v}$  in every CG iteration. The calculation of the right-hand side is straightforward. However, the matrix-vector product requires efficient computations of the  $L_2$  inner product of pairs of basis functions:

$$(\phi_{l,i}, \phi_{l',i'})_{L_2} = \int_{\Omega} \phi_{l,i}(\mathbf{x}) \phi_{l',i'}(\mathbf{x}) d\mathbf{x} \quad (13)$$

$$= \int_0^1 \phi_{l_1, i_1}(x_1) \phi_{l'_1, i'_1}(x_1) dx_1 \cdots \int_0^1 \phi_{l_d, i_d}(x_d) \phi_{l'_d, i'_d}(x_d) dx_d. \quad (14)$$

The 1d integrals can be computed directly:

$$\int_0^1 \phi_{l,i}(x) \phi_{l',i'}(x) dx = \begin{cases} \frac{2}{3} h_l, & x_{l_i} = x_{l'_i}, \\ h_l \phi_{l,i}(x_{l'_i}) + h_l \phi_{l',i'}(x_{l_i}), & \text{else.} \end{cases} \quad (15)$$

127 We note that in many instances the integral will be zero due to the non-overlapping support of the hat  
128 functions.

129 Figure 4 shows the effect of varying the regularization parameter  $\lambda$  for a 2d dataset.  $\lambda$  has to be  
130 chosen with care, as too small values might split a single cluster into multiple clusters. On the other  
131 hand, if  $\lambda$  is too large separate clusters could be part of the same high-density region.

### 132 3.3. Streaming Algorithms for the Sparse Grid Density Estimation

133 A high-performance sparse grid density estimation algorithm needs to efficiently compute the  
134 two operations described above: the computations of the matrix-vector product  $\mathbf{v}' = (B + \lambda I)\mathbf{v}$  and the  
135 right-hand side  $\mathbf{b}$  with  $b_i = \frac{1}{m} \sum_{j=1}^m \phi_i(\mathbf{x}_j)$ . To efficiently perform these operations, we use an implicit  
136 matrix approach. That is, components of the matrix  $B$  get re-computed whenever they are accessed.

137 This approach might seem wasteful at first glance. However, as the size of  $B$  scales quadratically in the  
 138 number of grid points, it quickly becomes infeasible to store the matrix in memory.

139

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**Algorithm 1:** The streaming algorithm for computing the right-hand side  $\mathbf{b}$

---

```

for  $i = 0 \dots N$  do
   $b_i \leftarrow 0$ 
  for  $j = 0 \dots m$  do
     $b_i += \prod_{d=1}^{\dim} \phi_i^{(d)}(x_j^{(d)})$ 
   $b_i \leftarrow \frac{1}{m} b_i$ 

```

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**Algorithm 2:** The streaming algorithm for computing the matrix-vector multiplication  $\mathbf{v}' = (B + \lambda I)\mathbf{v}$

---

```

for  $i = 0 \dots N$  do
   $v'_i \leftarrow 0$ 
  for  $j = 0 \dots N$  do
     $v'_i += \prod_{d=1}^{\dim} \int_0^1 \phi_i^{(d)} \phi_j^{(d)} dx \cdot v_j$ 
   $v'_i += \lambda \cdot v_i$ 

```

---

140 The computation of the right-hand side requires the computation of  $m$  vector components.  
 141 Algorithm 1 shows the loop structure of a scalar version of this operation. As the basis function  
 142 evaluations in the innermost loop are independent, we can parallelize this algorithm over the outer  
 143 loop, *i.e.* the iteration over the grid points. The evaluation of hat functions is branch-free. Therefore,  
 144 this algorithm is well-suited for vectorization.

145 We can formulate the matrix-vector operation as a second streaming algorithm with two nested  
 146 loops over the grid points. This is shown in Algorithm 2. Similar to the hat function evaluations of  
 147 the right-hand side, the  $L_2$  norms can be independently computed as well. Thus, we can parallelize  
 148 Algorithm 2 by processing the outer loop in parallel. The two cases for computing the 1d integral  
 149 according to Eq. 15 slightly complicate vectorization. Our approach is the computation of both cases  
 150 in a vectorized algorithm and a single conditional move to return the correct result. Computing  
 151 the  $x_{li} = x_{l'i'}$  case is only a single multiplication, as we can move the computation of  $h_l = 2^{-l}$  to  
 152 a precomputation step. Therefore, and because the  $x_{li} = x_{l'i'}$  case rarely occurs, the overhead in  
 153 each integration step is low compared to an optimal algorithm that would only evaluate the correct  
 154 integration case.

#### 155 4. Other Steps

156 In this section, we first present the algorithm for computing the  $k$ -nearest-neighbor graph. Then,  
 157 we show how we apply the sparse grid density estimation to prune it. Finally, we briefly describe how  
 158 we perform the connected component search in the pruned graph.

##### 159 4.1. Computing the $k$ -Nearest-Neighbor Graph

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**Algorithm 3:** A variant of the  $\mathcal{O}(m^2)$   $k$ -nearest-neighbor algorithm that uses  $b$  bins.

---

```

Input : dataset  $T = \{\mathbf{x}_i \in [0, 1]^d\}_{i=1}^m$ 
Output:  $k$ -nearest-neighbor graph  $g$  as neighborhood list
for  $c = 1 \dots b$  do
   $\text{dists}_c \leftarrow 0$ 
for  $i = 1 \dots m$  do
   $c \leftarrow 0$  //  $c$  iterates over the number of bins
  for  $j = 1 \dots m$  do
     $\text{dist} \leftarrow \text{distance}(\mathbf{x}_i, \mathbf{x}_j)$ 
    if  $\text{dist} < \text{dists}_{c+1}$  then
       $\text{bins}_{c+1} \leftarrow j$ 
       $\text{dists}_{c+1} \leftarrow \text{dist}$ 
     $c \leftarrow (c + 1) \bmod b$ 
   $g_i \leftarrow \text{extract\_nearest\_k}(\text{dists}, \text{bins}, i)$ 

```

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160 To create the  $k$ -nearest-neighbor graph, we have developed an approximate variant of the  $\mathcal{O}((k +$   
 161  $d)m^2)$  algorithm that compares all pairs of data points. Instead of creating a neighborhood list with  
 162  $k$  entries directly, we employ an approach with  $b$  bins that implicitly splits the dataset into  $b$  ranges.  
 163 For every data point  $i$  the dataset is iterated. Thereafter, each bin contains the nearest neighbor of its  
 164 assigned range of data points. To obtain an approximate  $k$ -nearest-neighbor solution, the  $k$  indices  
 165 with the smallest associated distances are selected from the  $b$  bins. Pseudocode for this approach is  
 166 displayed in Algorithm 3.

167 This  $k$ -nearest-neighbor algorithm offers several advantages. It is not affected by the curse of  
 168 dimensionality and therefore works well for the higher-dimensional datasets we target. In contrast,  
 169 spatial partitioning approaches such as  $k$ - $d$ -trees tend to suffer from the curse of dimensionality.  
 170 Furthermore, it maps well to modern hardware architectures as it is straightforward to parallelize and  
 171 vectorize. Through cache blocking of the outer loop that iterates  $i$ , the resulting algorithm is highly  
 172 cache-efficient as well. Finally, the number of bins  $b$  is the only parameter to specify.

173 Binning was introduced for performance reasons. It allows us to only perform a single comparison  
 174 in the innermost loop instead of  $k$  comparisons and, therefore, reduces the complexity to  $\mathcal{O}(dm^2)$ . The  
 175 effect on the detected clusters is minimal, as it is very likely that nodes are still connected to close-by  
 176 nodes of the same density region and therefore the same cluster. Furthermore, edges that intersect  
 177 low-density regions get pruned, as we describe in the next section.

178 The overall clustering algorithm is relatively robust with regard to different values of  $k$ . However,  
 179  $k$  should not be too small. Otherwise, the  $k$ -nearest-neighbor graph might be split into more connected  
 180 components than are desired. For larger values of  $k$ , performance decreases slightly in the subsequent  
 181 pruning step as the pruning algorithm has linear complexity in  $k$ . In our experience, choosing  $k$  with  
 182 values between five and ten balances this trade-off. Consequently, we set  $b$  to 16, as it is larger than  
 183 expected values of  $k$  and leads to a good-enough approximation of the  $k$ -nearest-neighbor graph.  
 184 On modern hardware platforms, this choice of  $b$  should not increase the cache or register memory  
 185 requirements of the algorithm to an extent that would affect performance.

#### 186 4.2. Pruning the $k$ -Nearest-Neighbor Graph

---

**Algorithm 4:** A streaming algorithm for pruning low-density nodes and edges of the  $k$ -nearest-neighbor graph. The density function is evaluated at the location of the nodes and at the midpoints of the edges.

---

**Input** :  $k$ -nearest-neighbor graph  $g$  as neighborhood list, dataset  $T$ ,  
 density estimation  $\hat{f}(\mathbf{x}) = \sum_j^N \alpha_j \phi_j(\mathbf{x})$ , threshold  $t$

**Output:** pruned  $k$ -nearest-neighbor graph  $g$

```

for  $i = 0 \dots m$  do
  if  $\hat{f}(\mathbf{x}_i) < t$  then
    prune_node( $g_i$ )
    continue
   $\mathbf{p}_1, \dots, \mathbf{p}_k \leftarrow \text{load\_midpoints}(T, g_i)$ 
  for  $j = 1 \dots k$  do
    if  $\hat{f}(\mathbf{p}_j) < t$  then
      prune_edge( $g_{i,j}$ )
  
```

---

187 To prune the  $k$ -nearest-neighbor graph, we use two criteria. The density function is evaluated at  
 188 the position  $\mathbf{x}_i$  that corresponds to the current graph node  $g_i$ . If the density is below a threshold  $t$ , the  
 189 node and its edges are removed. Furthermore, we evaluate at the midpoints of all outgoing edges and  
 190 prune all edges where the density is below  $t$ . By evaluating the midpoints, clusters can be successfully  
 191 separated even if two data points are in high-density regions that belong to different clusters. Our  
 192 pruning approach is shown in Algorithm 4.

193 Similar to the other algorithms presented, iterations of the outer loop are independent and can  
194 therefore be parallelized. The most expensive operations in this loop, multiple evaluations of the  
195 density function, are branch-free. Therefore, this algorithm is straightforward to vectorize as well.  
196 As there are only  $\mathcal{O}(m \cdot (k + 1))$  conditionals compared to overall  $\mathcal{O}(m(k + 1)N)$  operations, the  
197 conditionals do not significantly impact performance.

#### 198 4.3. Connected Component Detection

199 To detect the weakly connected components in the pruned graph, we first convert the directed  
200 graph to an undirected graph by adding all inverted edges. Then, we perform a depth-first search  
201 to detect the connected components. This classical algorithm performs  $\mathcal{O}(km)$  memory operations.  
202 Because the complexity of this algorithm is significantly lower compared to all other steps, this  
203 algorithm is only shared-memory parallelized and not distributed.

### 204 5. Implementation

205 In this section, we describe how our sparse grid clustering approach was implemented. To that  
206 end, we first consider the OpenCL-based node-level implementation and then present our distributed  
207 manager-worker approach.

#### 208 5.1. Node-Level Implementation

209 Except for the connected component detection, all steps of the clustering algorithm have been  
210 implemented as OpenCL kernels. There are two OpenCL kernels for the density estimation: one for  
211 calculating the right-hand side and one for the matrix-vector multiplication. A third OpenCL kernel  
212 implements the  $k$ -nearest-neighbor graph creation and a fourth kernel implements the density-based  
213 graph pruning.

214 From a performance engineering perspective, these OpenCL kernels have some commonalities.  
215 All kernels were parallelized over the outermost loop, exploiting the fact that the loop iterations are  
216 independent. Furthermore, all OpenCL kernels were designed to be branch-free. The only exception is  
217 the density matrix-vector multiplication kernel that has a single branch in the innermost loop. This  
218 branch is implemented using the OpenCL *select* function to differentiate between the integration cases.  
219 On modern OpenCL platforms, this should be compiled to a conditional move. Because only standard  
220 arithmetic is used and because of the regular control flow, the four compute kernels get vectorized on  
221 all OpenCL platforms we tested. Due to the design of the compute kernels, we expect this to be the  
222 case on many untested OpenCL platforms as well.

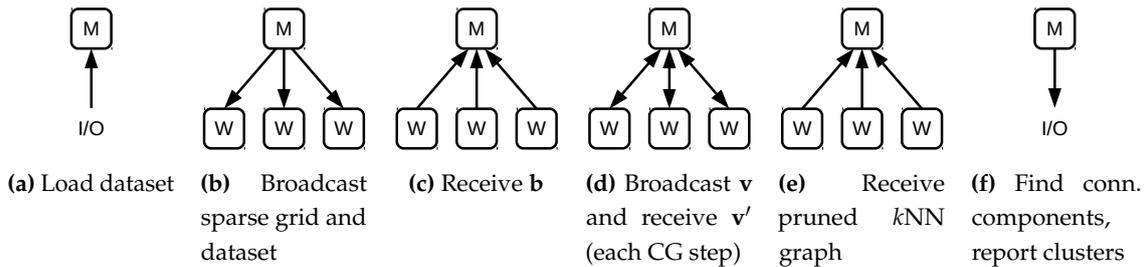
223 The local memory is used in all kernels to either share grid points or data points between all  
224 threads of the work-group. For example, the prune graph kernel evaluates 1d sparse grid basis  
225 functions in its innermost loop. The threads of a work-group process different data points, but all  
226 always evaluate their data point with the same basis function. Therefore, the data of the currently  
227 processed basis function can be shared efficiently through the local memory. Furthermore, the data  
228 point assigned to a thread remains constant throughout the lifetime of the thread. It can therefore be  
229 stored in *private* memory, which translates to the register file on GPU devices and the L1 cache (or the  
230 registers) on processors.

231 Table 1 shows the number of floating-point operations for the different OpenCL kernels. As both  
232 the number of grid points  $N$  and the size of the dataset  $m$  can be large, all operations are potentially  
233 expensive. In most data mining scenarios, the sparse grid will have significantly fewer grid points  
234 than there are data points. Therefore, the  $k$ -nearest-neighbor graph creation is expected to be the most  
235 expensive operation. Depending on the number of CG iterations, the density matrix-vector product  
236 can be moderately expensive as well. However, it only depends on the grid points and therefore  
237 benefits from  $N < m$ .

238 To estimate the achievable performance of our compute kernels, we calculated the arithmetic  
239 intensities of our compute kernels. As Tab. 1 shows, the arithmetic intensities of a work-group with a

Kernel	FP ops./complexity	Arith. int. (ws=1)	Arith. int. (ws=128)	Peak lim. (%)
density right-hand side	$N \cdot m \cdot d \cdot 6$	$1.5 \text{ FB}^{-1}$	$192 \text{ FB}^{-1}$	67%
density matrix-vector	$\text{CG-iter.} \cdot N^2 \cdot d \cdot 14$	$1.2 \text{ FB}^{-1}$	$149 \text{ FB}^{-1}$	64%
create graph	$m^2 \cdot d \cdot 3$	$1.0 \text{ FB}^{-1}$	$129 \text{ FB}^{-1}$	83%
prune graph	$m \cdot N \cdot (k+1) \cdot d \cdot 6$	$4.5 \text{ FB}^{-1}$	$576 \text{ FB}^{-1}$	67%

**Table 1.** The number of floating-point operations for the different OpenCL kernels and the arithmetic intensities (in floating point operations per byte) for a work-group size (ws) of one thread and 128 threads. The peak limit states the achievable fraction of the peak performance given the instruction mix of the compute kernels.



**Figure 5.** The distributed clustering algorithm from the perspective of the manager node. The (inexpensive) assignment of index ranges is not shown.

240 single thread would be too low to achieve a significant fraction of the peak performance on modern  
 241 hardware platforms (see Tab. 2 for the machine balances of the hardware platforms we used). However,  
 242 with a larger work-group size of 128 threads, and because we efficiently use the shared memory,  
 243 the arithmetic intensity is strongly improved. As a consequence, memory accesses do not limit the  
 244 performance of these compute kernels on modern hardware platforms. On processor-based platforms,  
 245 the L1 cache enables similarly-high arithmetic intensity values.

246 The arithmetic intensity values would allow our compute kernels to achieve peak performance.  
 247 However, as our compute kernels make use of instructions other than fused-multiply-add (FMA)  
 248 operations, the instruction mix reduces the achievable performance. To calculate the peak limit given  
 249 in Tab. 1, we make the (realistic) assumption that the remaining vector floating-point instructions run  
 250 at half the performance of the FMA instructions [29].

## 251 5.2. Distributed Implementation

252 For distributed computing, we developed a manager-worker model that was implemented with  
 253 MPI. To create work that can be assigned to the workers, we split the loops that were used for  
 254 parallelization (the outermost loops of the compute kernels) once again. We use a static load balancing  
 255 scheme that distributes the work equally to the workers. Our implementation supports single as well  
 256 as double precision. Currently, we transfer double precision data even if single precision is used in the  
 257 compute kernels.

258 From the perspective of the manager node, the distributed algorithm consists of four major steps:  
 259 creating the right-hand side of the density estimation, the density matrix-vector products, an integrated  
 260 graph-creation-and-prune step and the connected component search. These steps are shown in Fig. 5.  
 261 Note that the matrix-vector multiplication step (Fig. 5d) is repeated once per CG iteration.

262 When the application is started, the dataset is read by the manager node and sent to all workers,  
 263 requiring  $m \cdot d \cdot 8 \text{ B}$  of communication per worker. Then, the manager node creates the grid and sends it  
 264 to the workers as well. This requires  $2 \cdot N \cdot d \cdot 8 \text{ B}$  per worker to be communicated. After these relatively  
 265 expensive transfers are completed, grid and dataset are held by the workers. Therefore, most remaining  
 266 communication steps only require small amounts of data to be transferred. We demonstrate in Sec. 6  
 267 that the overhead of these communication steps is indeed very low compared to the computational  
 268 requirements of the remainder of the algorithm.

Device	Type	Cores/ Shaders	Frequency	Peak (SP)	Mem. Bandw.	Machine Balance
Tesla P100	GPU	3584	1.3 GHz (boost)	9.5 TF	720 GB s <sup>-1</sup>	12.9 FB <sup>-1</sup>
FirePro W8100	GPU	2560	0.8 GHz (max)	4.2 TF	320 GB s <sup>-1</sup>	13.2 FB <sup>-1</sup>
2xXeon E5-2680v3	CPU	24	2.5 GHz (base)	1.9 TF	137 GB s <sup>-1</sup>	14.0 FB <sup>-1</sup>

**Table 2.** The hardware platforms used in the distributed and node-level experiments. We list the frequency type that best matches our observations during the experiments.

269 To compute the density right-hand-side operation, every worker computes an index range of the  
 270 components of  $\mathbf{b}$ . As  $\mathbf{b}$  is aggregated on the manager node,  $N \cdot 8$  B need to be transferred. During each  
 271 CG step and after the final CG step, the manager sends  $\mathbf{v}$  ( $\alpha$  after the final iteration) to all workers.  
 272 Each worker calculates the result of an index range of  $\mathbf{v}'$  and communicates the partial result back to  
 273 the manager node. Therefore,  $N \cdot 8$  B per worker are communicated during each iteration and after the  
 274 final iteration. Collecting the partial results for  $\mathbf{v}'$  requires another  $N \cdot 8$  B to be transferred per CG  
 275 step.

276 The creation of the  $k$ -nearest-neighbor graph only requires the assignment of index ranges and no  
 277 further communication. Because the pruning of the  $k$ -nearest-neighbor graph reuses the same index  
 278 ranges that were assigned in the graph creation step, this step only requires the pruned graph to be  
 279 sent to the manager node. This step requires  $k \cdot m \cdot 8$  B to be transferred. Having received the pruned  
 280 graph, the manager node performs the connected component detection and has thereby computed the  
 281 clusters.

## 282 6. Results

283 In this section, we evaluate our distributed and performance-portable sparse grid clustering  
 284 approach. We first present the hardware platforms and datasets that were used in the experiments.  
 285 Then we provide the results of our node-level experiments that demonstrate performance portability.  
 286 The quality of the clustering is discussed in the context of the node-level experiments as well. Finally,  
 287 we present distributed performance results for two supercomputers: Hazel Hen and Piz Daint.

### 288 6.1. Hardware Platforms

289 On the node level, we used three different hardware platforms. Two of them are GPUs: the  
 290 Nvidia Tesla P100 and the AMD FirePro W8100. To represent standard processor platforms, we used a  
 291 dual socket machine with two Intel Xeon E5-2680v3 processors. The relevant technical details of these  
 292 hardware platforms are summarized in Tab. 2.

293 Our distributed experiments were conducted on two supercomputers. The Cray XC40 Hazel Hen  
 294 is an Intel processor-based machine with 7712 nodes for a peak performance of 7.4 PF. Hazel Hen is  
 295 located at the High Performance Computing Center Stuttgart (HLRS) in Stuttgart, Germany. It has  
 296 dual socket nodes with Xeon E5-2680v3 processors and 128 GB of memory per node.

297 The Cray XC40/XC50 Piz Daint is a mostly GPU-based supercomputer with a peak performance of  
 298 27 PF. Piz Daint is located at the Swiss National Supercomputing Centre (CSCS) in Lugano, Switzerland.  
 299 Each of the XC50 nodes that we used have a single Intel Xeon E5-2690v3 processor with 64 GB of  
 300 memory and a single Nvidia Tesla P100 GPU. In our experiments, we only used the Tesla P100 to  
 301 compute the main compute kernels of our application.

### 302 6.2. Datasets and Experimental Setup

303 In all of our experiments, we used synthetic datasets with clusters drawn from Gaussian  
 304 distributions. The cluster centers  $\mu$  were drawn randomly. We normalized the datasets to  $[0.1, 0.9]^d$ . As  
 305 this moves data points sufficiently towards the center of the domain, we can use a sparse grid without  
 306 boundary grid points.

Name	Size	Clust.	$\sigma$	Dim.	Dist.	Noise	Type
10M-3C	10M	3	0.12	10	$3 \cdot \sigma$	0%	distributed
100M-3C	100M	3	0.12	10	$3 \cdot \sigma$	0%	distributed
1M-10C	1M	10	0.05	10	$7 \cdot \sigma$	2%	node-level
1M-100C	1M	100	0.05	10	$7 \cdot \sigma$	2%	node-level
10M-100C	10M	100	0.05	10	$7 \cdot \sigma$	2%	node-level

**Table 3.** The Gaussian datasets for the distributed and the node-level runs

Name	$\lambda$	Threshold $t$	Level	Grid Points	CG $\epsilon$	$k$	ARI	Type
1M-10C	1E-5	667	6	76k	1E-2	6	1.0	node-level
1M-100C	1E-6	556	7	0.4M	1E-2	6	0.85	node-level
10M-10C	1E-5	1167	7	0.4M	1E-2	6	1.0	node-level
10M-100C	1E-6	1000	7	0.4M	1E-2	6	0.90	node-level
10M-3C	1E-6	$0.7 \cdot \max(\alpha)$	7	0.4M	1E-3	5	-	distributed
100M-3C	1E-6	$0.7 \cdot \max(\alpha)$	8	1.9M	1E-3	5	-	distributed

**Table 4.** The parameters used for configuring the clustering algorithm and the adjusted Rand index (ARI) for the node-level experiments. In the distributed runs, the threshold was specified as a fraction of the maximum surplus of the density function. The node-level runs used an absolute threshold value.

307 The parameters used to generate the datasets are listed in Tab. 3. The datasets with 100 clusters  
 308 are challenging, as the density estimation needs to correctly separate 100 high-density regions in a  
 309 moderately-high dimensional setting. Furthermore, to make it possible to assess the quality of the  
 310 clustering, we generated the node-level dataset so that the clusters are well-separated by forcing a  
 311 minimum distance of  $7 \cdot \sigma$  between the cluster centers. We verified that the noise connects all clusters  
 312 in the unpruned  $k$ -nearest-neighbor graph.

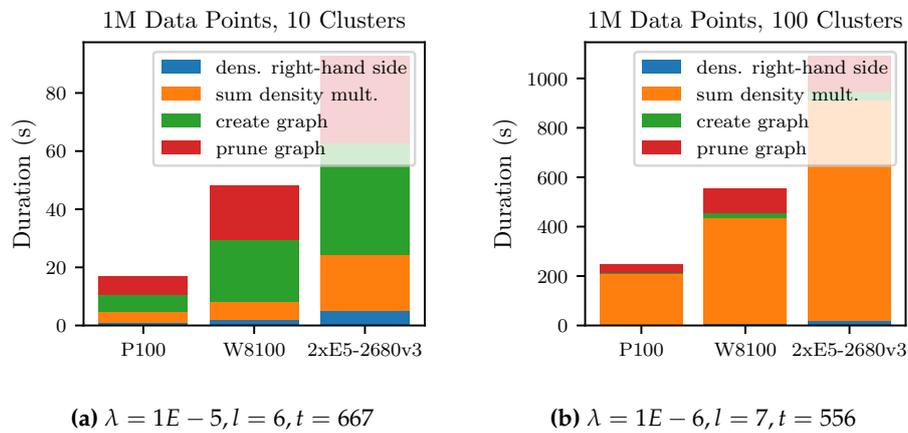
313 As the clustering algorithm requires parameterization as well, these parameters are shown in  
 314 Tab. 4. The adjusted Rand index (ARI) is a quality measure for clustering and is addressed in Sec. 6.4.  
 315 In all of our experiments, we used single-precision floating-point arithmetic.

### 316 6.3. Node-Level Performance and Performance-Portability

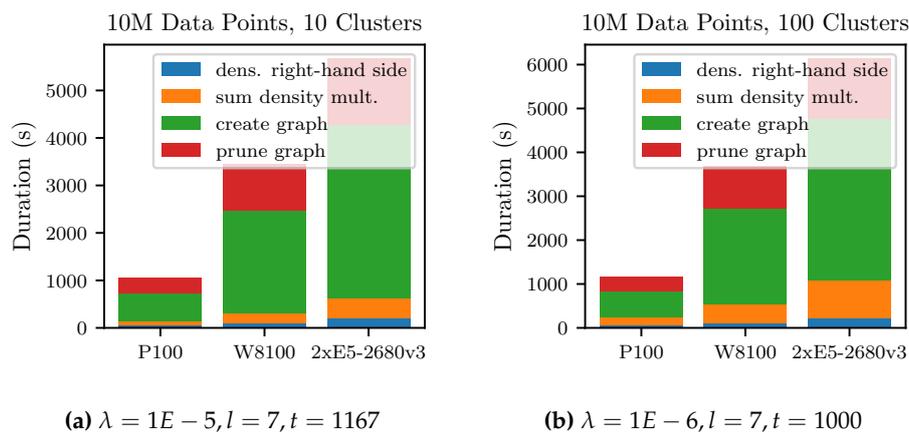
317 The tables 6 and 7 show the runtimes of the node-level experiments. For more consistent results,  
 318 the runs were repeated four times and the measurements averaged. The 1M-10C dataset could be  
 319 processed on a Tesla P100 in less than 20 s. Processing the 1M-100C dataset is more time-consuming  
 320 and required 248 s again using a Tesla P100. The main reason for the time increase is because the  
 321 density estimation requires more time due to a larger sparse grid and a smaller  $\lambda$ , which leads to more  
 322 CG iterations.

		Tesla P100	FirePro W8100	2xE5-2680v3
dens. right-hand side	GFLOPS	4584	2271 (753 MHz)	1177
limit: 67% peak	peak (of lim.)	48% (72%)	59% (88%)	61% (91%)
dens. matrix-vector	GFLOPS	4090	1939 (759 MHz)	919
limit: 64% peak	peak (of lim.)	43% (67%)	50% (78%)	48% (75%)
create graph	GFLOPS	5474	1433 (467 MHz)	852
limit: 83% peak	peak (of lim.)	58% (70%)	60% (72%)	44% (53%)
prune graph	GFLOPS	5360	1817 (822 MHz)	1265
limit: 67% peak	peak (of lim.)	56% (84%)	43% (64%)	66% (99%)

**Table 5.** The node-level performance of the clustering algorithm. All results are for single-precision arithmetic. The performance was measured with the 10M-10C dataset and the parameters listed in Tab. 4. Note that the achievable peak performance is limited by the instruction mix to values significantly below 100%.



**Figure 6.** The duration of the node-level experiments with one million data points. Because the 1M-100C dataset requires a larger grid, the density estimation takes up most of the overall runtime.



**Figure 7.** The duration of the node-level experiments with 10 million data points

323 The experiments with the 10 million data points datasets are shown in Tab. 7. Due to the increased  
324 size of the datasets, the  $k$ -nearest-neighbor graph creation takes up the largest fraction of the runtime  
325 in both experiments. This illustrates that for large datasets, because of its quadratic complexity, the  
326  $k$ -nearest-neighbor graph creation step will dominate the overall runtime. In these two experiments,  
327 increasing the number of clusters has only a small effect on the runtime. Mainly, because in both  
328 cases a sparse grid with level  $l = 7$  was used. On the P100 platform, the experiments with the  
329 10M-100C dataset took 1162 s. The other hardware platforms took longer, proportional to their lower  
330 raw performance.

331 Table 5 shows the performance achieved in the node-level experiments. It displays the  
332 performance in GFLOPS and the achieved fraction of peak performance. The achieved fraction  
333 of the peak performance relative to the instruction-mix-based limit is displayed as well. These results  
334 were calculated from the runs with the 10M-10C dataset as specified in Tab. 4.

335 Our implementation achieved a significant fraction of the peak performance across all devices.  
336 Additionally, if the limit imposed by the instruction mix is taken into account, we see that many  
337 combinations of kernels and devices run close to their maximally achievable performance. The only  
338 kernel that reaches less than two-thirds of its achievable performance is the create graph kernel on  
339 the Xeon E5 platform. We suspect that this is due to throttling of the processor, as this operation puts  
340 extreme stress on the vector units.

341 The fastest device by a significant margin is the Tesla P100, as it is the most recent of the devices and  
342 has the highest theoretical peak performance. It is 2.23 – 3.29x faster than the W8100 and 4.41 – 5.49x  
343 faster than the Xeon E5 pair.

344 The FirePro W8100 achieves similar fractions of the peak performance compared to the P100 at a  
345 lower absolute level of performance. It is still 1.67 – 1.98x faster than the pair of Xeon E5 processors.  
346 During our experiments, the FirePro W8100 displayed strong throttling which is why we list the  
347 average frequencies observed for the individual compute kernels. The reduced frequencies imply  
348 lower achievable peak performance ( $2 \cdot 2560 \cdot f_{avr}$ ) which we take into account for the calculation  
349 of the peak performance and the resulting achieved fraction of peak performance. The average  
350 frequencies reported were measured in a separate run of the 10M-10C experiment. In case of the  
351  $k$ -nearest-neighbor graph kernel, a frequency of only 492 MHz was measured. This nearly halves the  
352 achievable performance of this compute kernel.

353 Because it has the lowest absolute performance, the pair of Xeon E5 processors scores lowest.  
354 However, the achieved fractions of the peak performance are similar to the other devices. This indicates  
355 that performance is not only portable across GPU platforms, but processor-based platforms as well.

#### 356 6.4. Clustering Quality and Parameter Tuning

357 This work mostly focuses on the performance of our sparse grid clustering approach. Nevertheless,  
358 to make our evaluation more realistic, we tuned the clustering parameters of our node-level runs for  
359 (nearly) optimal clustering quality. For a more detailed discussion of the achievable level of quality,  
360 we refer to prior work which compared sparse grid clustering to other clustering algorithms [21]. A  
361 comparison of the sparse grid density estimation to other density estimation methods is available as  
362 well [30].

363 To assess the quality, we used the adjusted Rand index (ARI) which compares two cluster  
364 mappings. Because we know the mapping of data points to clusters of each of our synthetic datasets,  
365 these reference cluster mappings were compared to the output of the sparse grid clustering algorithm.  
366 The calculated ARI of the node-level experiments is shown in Tab. 4. These results show that we  
367 can nearly perfectly reconstruct the clusters of both datasets with ten clusters. The datasets with 100  
368 clusters are more challenging and would require slightly larger grids for further improvements.

369 We used a parameter tuning approach to fit the computed cluster mappings to the reference  
370 mappings. During parameter tuning, we first select a value for the regularization parameter  $\lambda$  and  
371 then search for the best pruning threshold  $t$ . This was implemented as two nested binary searches.

372 To speed up parameter tuning in general, our sparse grid clustering implementation allows for  
373 reusing of the  $k$ -nearest-neighbor graph and calculated density estimations. As the  $k$ -nearest-neighbor  
374 graph is the same independent of all parameters, it can be calculated once overall. Moreover, the  
375 density estimation changes only if  $\lambda$  is changed. Thus, the density estimation can be reused while  
376 an optimal value for  $t$  is searched. Only the comparably cheap graph pruning operation and the  
377 connected component search are performed at every parameter tuning step.

#### 378 6.5. Distributed Results on Hazel Hen

379 Figure 8 shows the results of the distributed experiments conducted on Hazel Hen. Results are  
380 given for the individual compute kernels as well as the whole application run. The total runtime, and  
381 the average application TFLOPS derived from it, is based on the wall-clock time of the application and  
382 not only on the three major distributed operations. At the highest node count, it took 4226 s to process  
383 the 100M-3C dataset and 259 s to process the 10M-3C dataset. We achieved up to 100 TFLOPS for the  
384 100M-3C dataset using 128 nodes and up to 23 TFLOPS for the 10M-3C using 32 nodes. Therefore,  
385 we achieved 41% and 37% of the peak performance at the highest number of nodes for the whole  
386 application including all communication and file input-output operations.

387 The creation and pruning of the  $k$ -nearest-neighbor graph scales nearly linearly. Calculating the  
388 density estimation scales slightly worse. As the grid is much smaller than the dataset, there is too little  
389 work available per node during the density estimation step to achieve optimal performance at high  
390 node counts.

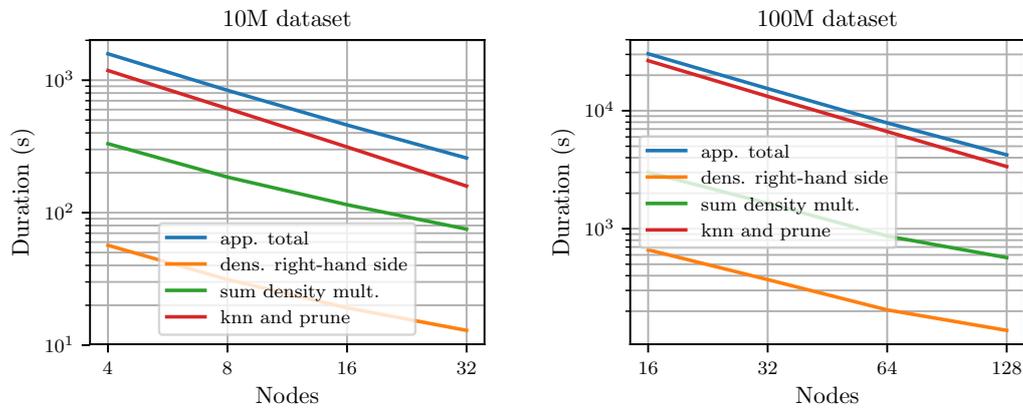
391 Figure 8c displays the duration of the initial loading and distribution of the dataset, the creation  
392 and the transfer of the sparse grid and the duration of the connected component search. As Fig. 8c  
393 shows, loading and communicating the dataset does not take up significant amounts of time. The  
394 same is true for creating and transferring the sparse grid. However, the connected component search  
395 becomes relatively expensive for the 100M-3C dataset, as it is performed on a single node and therefore  
396 cannot scale with an increasing number of nodes. Nevertheless, at 128 nodes the connected component  
397 search still only requires 107 s or 2.5% of the total runtime for the 100M-3C dataset. For the 10M-3C  
398 dataset and 32 nodes, the connected component search takes up 2.2% of the total runtime.

#### 399 6.6. Distributed Results on Piz Daint

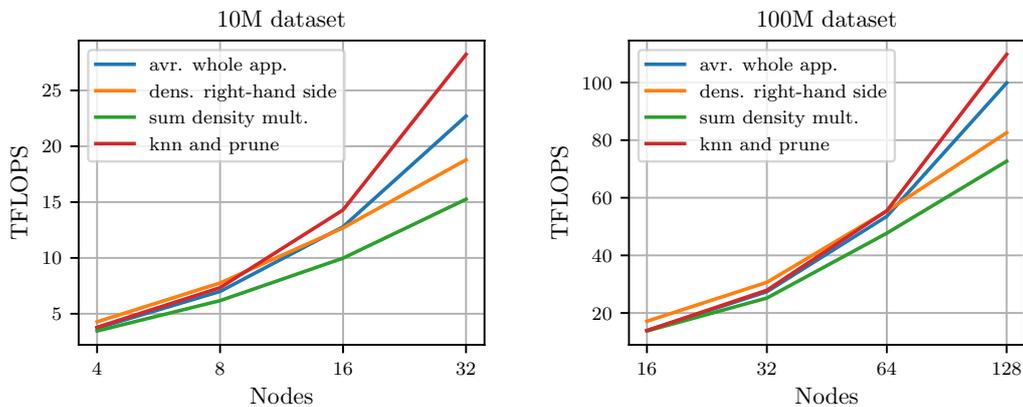
400 We conducted the distributed experiments before we were able to do some final node-level  
401 optimizations, and due to compute time limitations we were not able to recompute the experiments.  
402 Thus, the results of these experiments are not directly comparable to the node-level performance  
403 results. Since these experiments, the node-level performance of all compute kernels was improved.  
404 Because of this, scalability might be slightly overestimated. Furthermore, the duration of the connected  
405 component search is not listed in these results, as we used a different algorithm at the time of the  
406 experiments.

407 Figure 9 shows duration and performance of the experiments performed on Piz Daint for both  
408 the 10M-3C and 100M-3C datasets. Again, results are given for the individual compute kernels as well  
409 as the whole application run. As Fig. 9a shows, the application scales well to 128 nodes. Similar to  
410 the Hazel Hen results, the integrated graph-creation-and-prune step scales nearly linearly, whereas  
411 the density estimation scales slightly worse. Using 32 nodes, the clustering of the 10M-3C dataset  
412 takes 100 s. It takes 1198 s to cluster the 100M-3C dataset using 128 nodes. This translates to an average  
413 performance of 59 TFLOPS for the 10M-3C dataset and 352 TFLOPS for the 100M-3C dataset as Fig. 9b  
414 shows. Thus, at 128 nodes our implementation still achieves 29% of the peak performance for the  
415 whole application including all communication and the loading of the dataset.

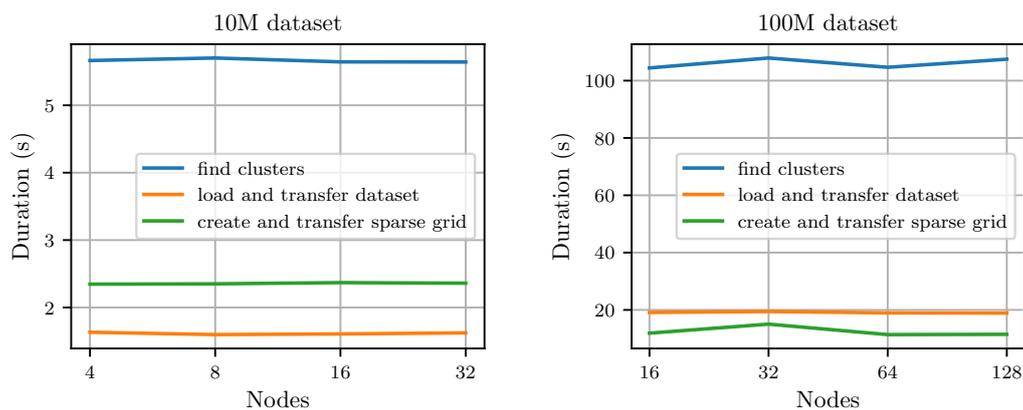
416 Figure 9c displays the duration of the initial loading and distribution of the dataset and the  
417 creation and transfer of the sparse grid to the workers. Only the loading of the dataset is somewhat  
418 expensive.



(a) The durations in seconds of the clustering experiments on Hazel Hen

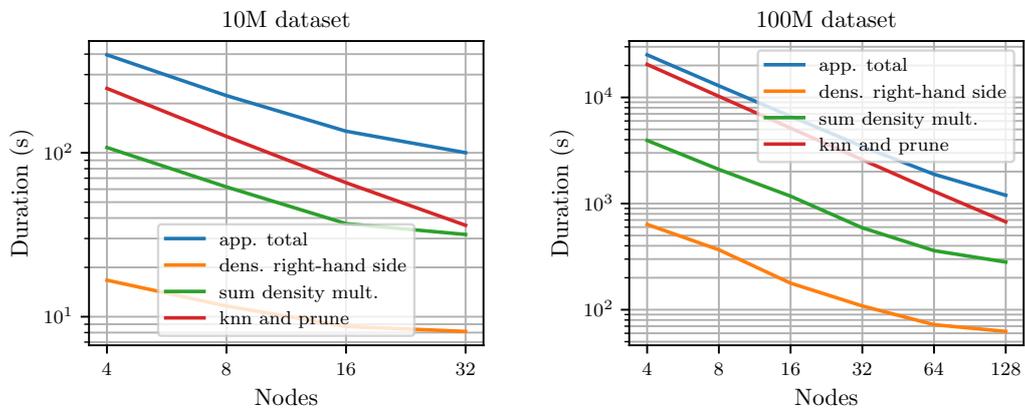


(b) The performance in TFLOPS of the clustering experiments on Hazel Hen

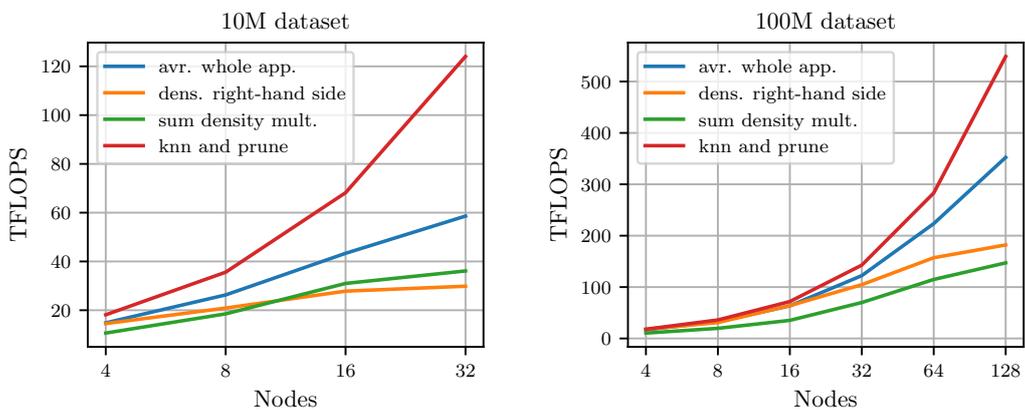


(c) The durations for loading the dataset, creating the sparse grid and transferring both to the workers. We additionally show the time needed to perform the connected component search.

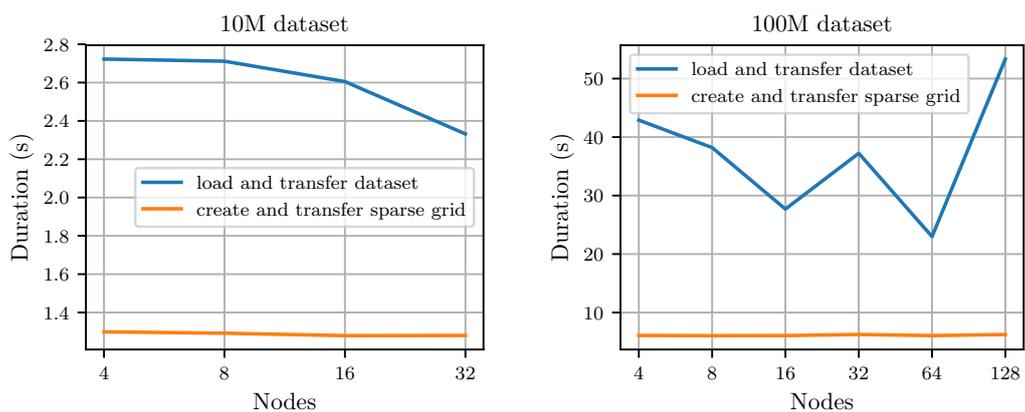
**Figure 8.** Strong scaling results for both the 10M (left graphs) and 100M (right graphs) Gaussian dataset on Hazel Hen. Figure 8a and Fig. 8b show duration and performance of the major compute kernels and the application as a whole. The duration of other (minor) tasks is shown in Fig. 8c.



(a) The durations in seconds of the clustering experiments on Piz Daint



(b) The performance in TFLOPS of the clustering experiments on Piz Daint



(c) The durations for loading the dataset, creating the sparse grid and transferring both to the workers.

**Figure 9.** Strong scaling results for both the 10M (left graphs) and 100M (right graphs) Gaussian dataset on Piz Daint. Figure 9a and Fig. 9b show duration and performance of the major compute kernels and the application as a whole. The duration of other (minor) tasks is shown in Fig. 9c.

419 Compared to Hazel Hen, the performance on Piz Daint is consistently higher, which is explained  
420 by the difference in node-level performance. However, due to the processor-based architecture, Hazel  
421 Hen nodes require less work per node to be fully utilized. Therefore, on Hazel Hen a slightly higher  
422 fraction of peak performance was achieved.

## 423 7. Discussion and Future Work

424 Sparse grid clustering, as implemented in the open source library SG<sup>++</sup>, is one the few clustering  
425 methods available for the clustering of large datasets on HPC machines. The underlying density  
426 estimation approach enables the detection of clusters with non-convex shapes and without a  
427 predetermined number of clusters. Due to the sparse grid discretization of the underlying feature  
428 space, the grid or discretization points are chosen independently of the data points. This is key to the  
429 linear complexity with respect size of the data of all sparse-grid-related algorithms. This property is  
430 highly useful for addressing big data challenges.

431 With our optimized implementation, we have demonstrated performance portability across  
432 three hardware platforms. Due to the use of OpenCL as the programming language, careful and  
433 highly-tuned performance optimization, and algorithms that map very well to the capabilities of  
434 modern hardware platforms, we expect similar performance on related platforms. Our strong scaling  
435 experiments show that even on 128 nodes of Piz Daint, scalability is mainly limited by the available  
436 work per node.

437 Our method achieves a significant fraction of the peak performance on all devices tested. This  
438 shows that OpenCL is a good choice for developing performance-portable software. Furthermore,  
439 our GPU results illustrate how the higher raw performance of GPUs in contrast to CPUs translates to  
440 similarly improved time-to-solution.

441 As our next steps, we plan to further improve the performance of our approach by addressing  
442 two key issues: First, the  $k$ -nearest-neighbor graph creation currently uses an  $\mathcal{O}(m^2)$  algorithm and  
443 thus represents the bottle-neck. We already have an early implementation of a GPU-enabled variant  
444 of the locality-sensitive hashing algorithm. The locality-sensitive hashing algorithm can calculate an  
445 approximate  $k$ -nearest-neighbor graph in sub-quadratic complexity [31]. Adopting this algorithm,  
446 sparse grid clustering can be performed in sub-quadratic complexity as well.

447 Second, our implementation supports the use of spatially-adaptive sparse grids [22,30]. They  
448 enable the placement of grid points only where they significantly contribute to the overall solution. An  
449 adaptive approach will significantly increase the dimensionality of the datasets that can be clustered as  
450 it has been demonstrated for standard learning tasks before. Currently, creating an adaptively-refined  
451 sparse grid is itself expensive as it requires the system of linear equations of the density estimation to be  
452 solved repeatedly after each refinement. Thus, a priori refinement strategies that create well-adapted  
453 sparse grids with less effort are another important direction of future research.

## 454 8. Materials and Methods

455 The source code of this study will be made available as part of the sparse grid toolbox SG<sup>++</sup> at the  
456 time of publication [32]. We archive the scripts for creating the synthetic datasets at the same location.

457 **Author Contributions:** Conceptualization, D.P, G.D. and D.Pf.; Methodology, D.P and G.D.; Software, D.P  
458 and G.D.; Validation, D.P and G.D.; Formal Analysis, D.P and G.D.; Investigation, D.P; Resources, D.P; Data  
459 Curation, D.P and G.D.; Writing–Original Draft Preparation, D.P; Writing–Review and Editing, D.P., G.D. and  
460 D.Pf.; Visualization, D.P.; Supervision, D.Pf.; Project Administration, D.Pf.; Funding Acquisition, D.Pf..

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466 **Conflicts of Interest:** The authors declare no conflict of interest.

467

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