

Technical Note

Not peer-reviewed version

Classical MPPT in a Partially Shaded PV String: Technical Note on System Operation and Control

[Pietro Perlo](#)*, Marco Dalmasso, Marco Biasiotto, Davide Penserini, Sergio Pozzato

Posted Date: 17 April 2026

doi: 10.20944/preprints202604.1226.v1

Keywords: photovoltaic; PV string; partial shading; fast-moving shadows; maximum power point tracking; MPPT; perturb and observe; incremental conductance; boost converter; bypass MOSFET; hot-spot prevention; digital control; battery charging; substring bypass



Preprints.org is a free multidisciplinary platform providing preprint service that is dedicated to making early versions of research outputs permanently available and citable. Preprints posted at Preprints.org appear in Web of Science, Crossref, Google Scholar, Scilit, Europe PMC.

Copyright: This open access article is published under a [Creative Commons CC BY 4.0 license](#), which permit the free download, distribution, and reuse, provided that the author and preprint are cited in any reuse.

Disclaimer/Publisher's Note: The statements, opinions, and data contained in all publications are solely those of the individual author(s) and contributor(s) and not of MDPI and/or the editor(s). MDPI and/or the editor(s) disclaim responsibility for any injury to people or property resulting from any ideas, methods, instructions, or products referred to in the content.

Technical Note

Classical MPPT in a Partially Shaded PV String: Technical Note on System Operation and Control

Pietro Perlo *, Marco Dalmasso, Marco Biasiotto, Davide Penserini and Sergio Pozzato

Interactive Fully Electrical VehicleS (IFEVS), La Loggia, Italy

* Correspondence: author: Pietro.Perlo@ifevs.com

Richard Feynman: “I decided to give these lectures because I understood that it was not so clear to the students and also to the professors what the important things were”.

Abstract

This report describes the practical implementation of a maximum power point tracking (MPPT) system for a string of four series-connected monocrystalline silicon photovoltaic (PV) cells. The system includes a **boost DC-DC converter** that interfaces the low-voltage PV string to a 36 V battery, and each cell has an individual MOSFET bypass switch to mitigate partial shading. We explain the operating principles, the MPPT algorithms (Perturb & Observe and Incremental Conductance), the bypass logic, and the closed-loop control that adjusts the converter’s duty cycle to maximise power transfer. Step-by-step descriptions and supporting figures clarify the process.

Keywords: photovoltaic; PV string; partial shading; fast-moving shadows; maximum power point tracking; MPPT; perturb and observe; incremental conductance; boost converter; bypass MOSFET; hot-spot prevention; digital control; battery charging; substring bypass

Acronym	Meaning
ADC	Analog-to-Digital Converter
CC	Constant-Current charging
CV	Constant-Voltage charging
DC-DC	Direct-Current to Direct-Current
IncCond	Incremental Conductance
MCU	Microcontroller Unit
MPP	Maximum Power Point
MPPT	Maximum Power Point Tracking
MOSFET	Metal-Oxide-Semiconductor Field-Effect Transistor
P&O	Perturb and Observe
PWM	Pulse-Width Modulation
PV	Photovoltaic
Rds(on)	MOSFET on-state resistance

Scope note: This technical note uses a teaching-scale demonstrator to explain classical PV-string MPPT under partial shading. The per-cell bypass architecture is retained for clarity of exposition; in commercial PV modules, bypass is usually implemented at substring level.

Introduction

Photovoltaic (PV) systems convert sunlight into electricity. When cells are connected in series, the string voltage increases, but a single shaded cell can limit the whole string's current and even become reverse-biased, dissipating power as heat (hot-spot risk). To prevent damage and recover energy, bypass elements are placed across parts of the string, and a maximum power point tracker (MPPT) continuously adjusts the operating point to maximise power.

In practice, commercial PV modules typically have one bypass diode (or an active MOSFET) per group of 20–24 cells. This substring-level bypass is sufficient because cells within a module are usually well matched and shading tends to affect whole sections. However, for educational purposes and to prepare for more advanced concepts (like spintronic per-cell management), we use one bypass MOSFET per cell in this demonstration. The principles remain the same: when a cell (or substring) becomes shaded, the bypass element turns on, allowing current to flow around it. The small-scale example with four cells makes the operation easy to follow and the equations compact.

Didactic simplification vs. real products:

- **What scales to commercial systems:** The MPPT algorithms, the boost converter control, the bypass logic, and the overall control loop are directly applicable.
- **What does not scale:** The per-cell bypass hardware; in real modules, bypass is applied per substring to minimise cost and complexity.

This technical note builds a complete digital MPPT system step by step. We will:

- Describe a single PV cell and its I-V and P-V characteristics.
- Show what happens when four cells are connected in series.
- Explain why a bypass element is needed and compare a Schottky diode with a MOSFET.
- Introduce MPPT and the role of a DC-DC converter (boost type).
- Detail the Perturb & Observe algorithm used by a microcontroller.
- Walk through the overall control loop and a practical example.

1. How a PV Cell Produces Voltage and Current

A solar cell is a semiconductor junction that converts light into electrical energy. Light creates charge carriers in silicon, and the junction's internal electric field separates them. Electrically, the cell behaves like a light-driven current source in parallel with a diode (the junction itself). Real cells also include small resistive losses.

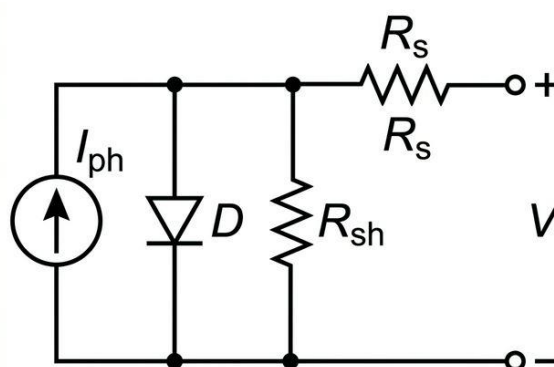


Figure 1. Conceptual PV cell model. This model shows the current source (I_{ph}) representing the light-generated current, the diode (D) representing the p-n junction, and parasitic series (R_s) and shunt (R_{sh}) resistances. This is a functional model, not a wiring schematic. Light mainly sets the available current; the diode and resistances shape the I-V curve.

If we connect the cell to different loads, we force it to operate at different voltages and currents. Plotting current versus voltage gives the I-V curve. Plotting power ($V \times I$) versus voltage gives the P-V curve. Under uniform illumination, the P-V curve has one peak: the maximum power point (MPP).

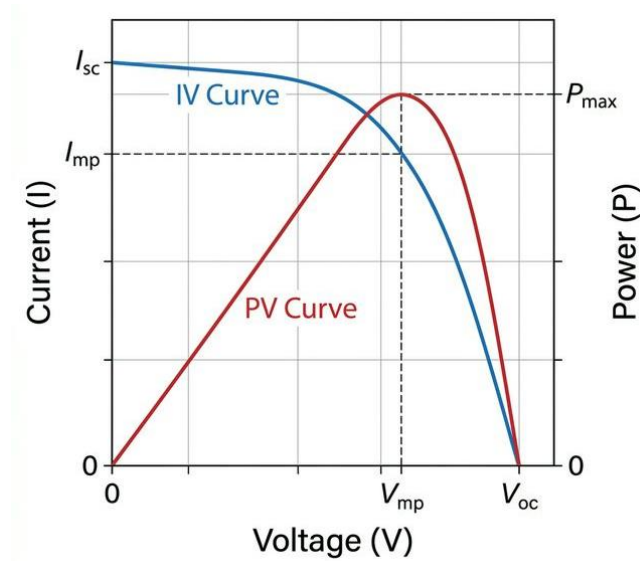


Figure 2. I-V and P-V characteristics of a single PV cell under uniform illumination. The I-V curve (blue) shows the classic shape, from short-circuit current (I_{sc}) to open-circuit voltage (V_{oc}). The P-V curve (red) is derived from it. The maximum power point (MPP) is the unique operating point (V_{mp} , I_{mp}) where the product $V \times I$ is at its peak. This point shifts with changes in sunlight and temperature.

The best operating point changes with sunlight and temperature, so a fixed load rarely keeps the PV at maximum power.

How a PV Cell Produces Voltage and Current

The output current I of a PV cell is described by the single-diode model:

$$I = I_{ph} - I_0 \left[\exp \left(\frac{V + IR_s}{nV_t} \right) - 1 \right] - \frac{V + IR_s}{R_{sh}}$$

- I_{ph} : photocurrent (proportional to irradiance)
- I_0 : diode saturation current
- R_s , R_{sh} : series and shunt resistances that shape the I-V curve
- n : ideality factor
- $V_t = kT/q$: thermal voltage

This equation is implicit because I appears on both sides. In practice, it is solved numerically or approximated for simulation.

2. What Changes When 4 Cells Are Connected in Series

When we connect cells in series, their voltages add, but the same current must flow through each one. A series string increases the usable voltage, but one weak cell can limit the whole string's current.

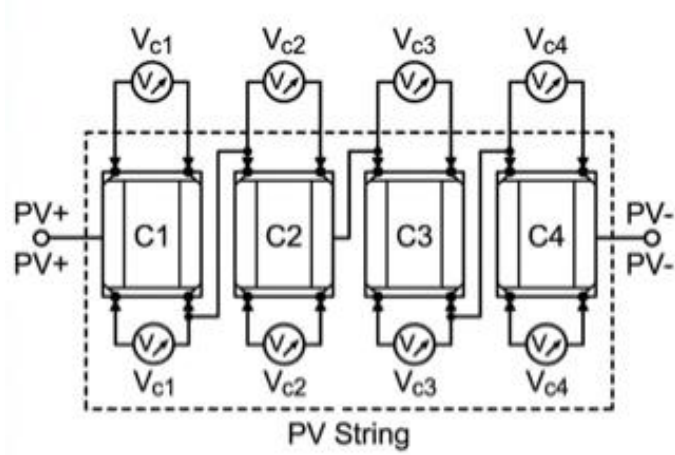


Figure 3. Four series cells forming one PV string. Four cells are connected in series, with their positive and negative terminals linked. We measure the per-cell voltages V_{c1} , V_{c2} , V_{c3} , and V_{c4} across each individual cell. Because the current is common throughout the series string, measuring each cell’s current is unnecessary. Per-cell voltage is the most informative signal: if one cell is shaded, its voltage tends to collapse first, acting as a “bottleneck.”.

3. What Happens If One Cell Is Under a Fast-Varying Shadow

Shadowing reduces the light on a cell, which reduces its available photocurrent. In a series string, the same current must flow through all cells, so the shaded cell becomes a bottleneck and pulls down the string current, unless there is a bypass path.

If the system forces current that the shaded cell cannot provide, that cell can be driven into reverse bias (negative cell voltage). In reverse bias, the cell dissipates power as heat (hot-spot risk) instead of generating power.

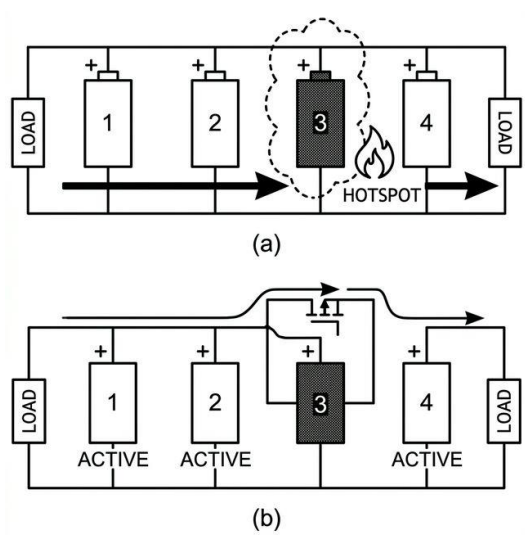


Figure 4. The effect of bypassing a shaded cell. (a) Without a bypass, a shaded cell limits the entire string’s current and can be forced into reverse bias, creating a hot spot. (b) With a bypass path activated, current flows around the shaded cell. The other three cells can continue to operate near their maximum power point, and the shaded cell is protected from reverse bias.

Why fast shadows matter: If shading changes many times per second, the best configuration and the MPP can move rapidly. A slow controller can spend much of the time away from the optimum.

4. Why MPPT Is Useful

A battery, resistive load, or inverter input tends to force the PV voltage to a convenient value for the load, not necessarily the PV string's maximum-power voltage. This leaves energy unharvested.

An MPPT controller uses a DC-DC converter. By changing its switching duty cycle, the converter changes how much current it draws from the PV string. That changes the PV operating point until harvested power is maximized.

Boost mapping (very important): In a boost converter, changing duty cycle D changes the input resistance seen by the PV string, and therefore its voltage and current.

- Decrease $D \rightarrow$ PV sees higher input resistance $\rightarrow$ PV voltage tends to increase (move right on the I-V curve).

- Increase $D \rightarrow$ PV sees lower input resistance $\rightarrow$ PV voltage tends to decrease (move left on the I-V curve).

Throughout this guide, when we say “increase PV voltage”, for a boost converter it usually means “decrease D ”, and vice versa.

The DC-DC converter acts as a **smart impedance transformer**. For a **boost converter** operating in continuous conduction mode, the voltage relation is

$$V_{\text{bat}} = \frac{V_{PV}}{1-D} \Leftrightarrow V_{PV} = V_{\text{bat}} \cdot (1-D)$$

where D is the duty cycle (fraction of the switching period during which the switch is ON). The input resistance seen by the PV string is

$$R_{\text{eq}} = \frac{V_{PV}}{I_{PV}} = (1-D)^2 \cdot R_{\text{load}}$$

By adjusting D , the controller slides the operating point along the I-V curve until it reaches the coordinates $(V_{\text{mp}}, I_{\text{mp}})$ that maximise power.

The PV string delivers maximum power when its operating point satisfies

$$\frac{dP}{dV} = 0 \Rightarrow \frac{dP}{dV} = I + V \frac{dI}{dV} = 0 \Rightarrow \frac{I}{V} = -\frac{dI}{dV}.$$

The boost converter's input resistance $R_{\text{eq}} = (1-D)^2 R_{\text{load}}$ is the only tunable parameter available to the MCU. By varying D , the controller forces the PV string to the point where the instantaneous conductance equals the incremental conductance—the exact condition that defines the MPP. This is why the two classic algorithms (P&O and IncCond) both manipulate D rather than voltage or current directly.

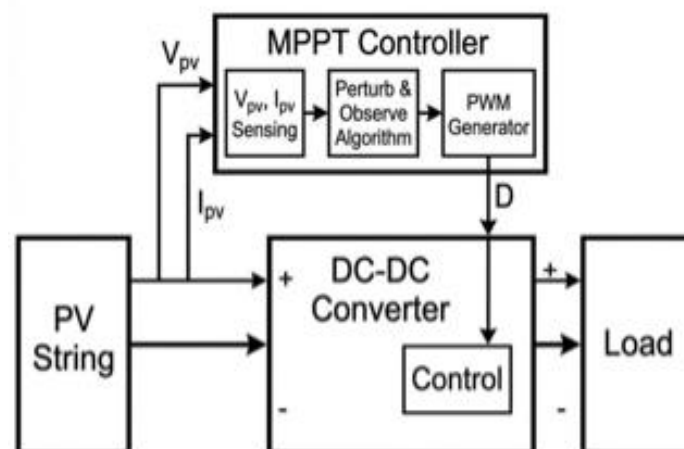


Figure 5. The role of an MPPT controller and boost converter. The PV string has a unique MPP. The load (e.g., a battery) has its own voltage requirement. The MPPT controller continuously adjusts the boost converter's duty cycle (D).

This makes the converter appear as an optimal impedance to the PV string, forcing it to operate at V_{mp} and I_{mp} . The converter then transforms this power to the voltage and current needed by the load.

5. Bypass Options: Schottky Diode vs MOSFET

A bypass element is placed across each cell so current can flow around a shaded cell when needed. Two common choices are a Schottky diode (passive) or a MOSFET-based bypass (lower loss but more complex).

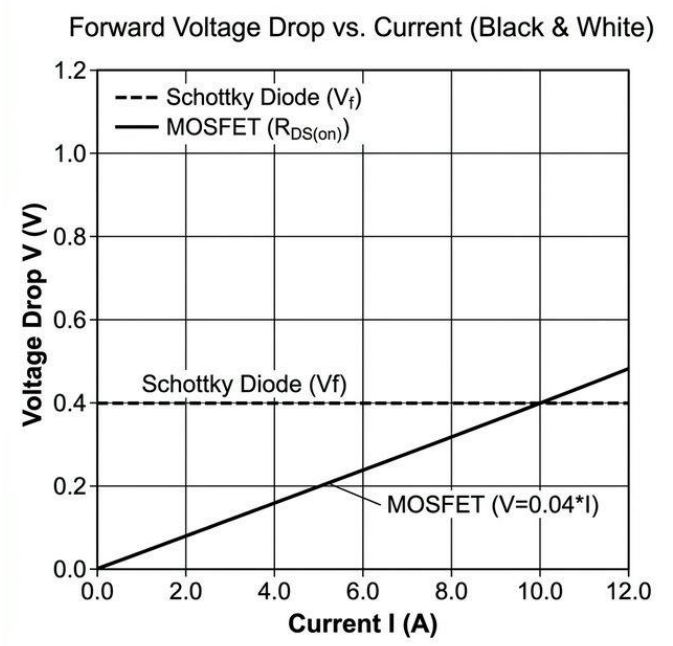


Figure 6. Comparison of voltage drop during bypass. A Schottky diode has a relatively constant forward voltage drop (V_f), typically 0.3–0.5 V. When carrying a high current (I), the power loss ($I \times V_f$) can be significant. A MOSFET, when fully turned on, behaves like a small resistor ($R_{ds(on)}$). Its voltage drop is $I \times R_{ds(on)}$, which can be much smaller than a diode’s V_f , especially for high-performance MOSFETs. This leads to lower power loss and cooler operation.

Feature	Schottky Bypass Diode	MOSFET Bypass (Ideal)
How it turns on	Automatically when forward-biased (cell voltage < - V_f)	By controller (or ideal-diode IC) when cell voltage is near zero
Loss while bypassing	$\approx I \cdot V_f$ (can be significant at high currents)	$\approx I^2 \cdot R_{ds(on)}$ (often much smaller)
Complexity	Low	Medium–high (requires gate drive and control logic)
Best use	Simple, robust safety path	Maximum efficiency under frequent or prolonged shading

If maximum harvested energy is the priority and shading is common, MOSFET bypass is usually better. Many designs still include a diode as a fail-safe parallel path.

6. System Overview

The system consists of:

- **Four series-connected PV cells** (SunPower monocrystalline silicon, each ~0.6 V open-circuit, ~6 A short-circuit under standard conditions).
- **Four MOSFET bypass switches** (one across each cell) to redirect current around a shaded cell.
- **A boost DC-DC converter** that steps up the low PV string voltage to charge a 36 V battery.
- **A 36 V lead-acid or lithium-ion battery** as the load.
- **A microcontroller (MCU)** that samples voltages and currents, runs the MPPT algorithm, and controls the MOSFET bypass switches and the DC-DC converter.

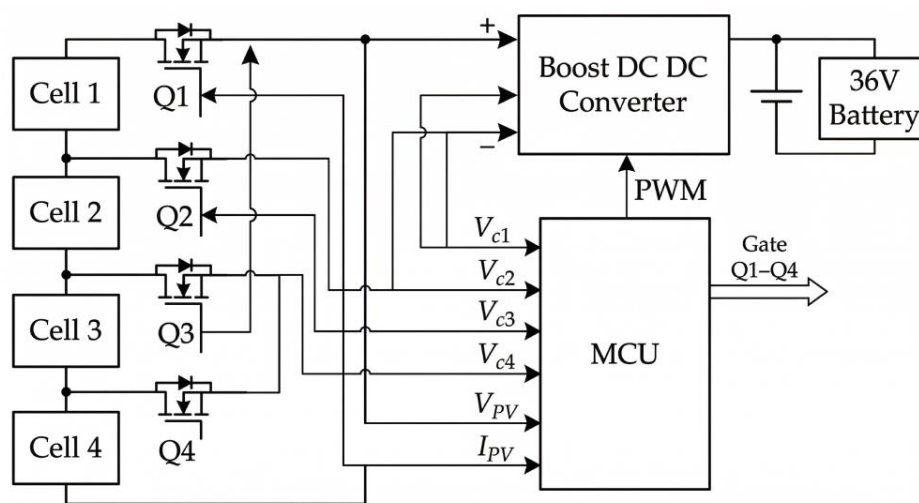


Figure 7. Block diagram of the MPPT system. Four PV cells are connected in series; each cell has a MOSFET bypass switch (Q1–Q4). The string voltage V_{PV} and current I_{PV} are measured and fed to the MCU. The MCU controls the boost converter via a PWM signal (duty cycle D) and drives the bypass MOSFETs. The converter output charges a 36 V battery.

7. PV String Characteristics

Under uniform illumination, the I-V and P-V curves of a single cell are as shown in Figure 2. For four cells in series, the string voltage is the sum of the individual cell voltages, while the string current is the same through all cells.

When one cell is shaded, its current-generating capability drops. If the string current is forced to be higher than the shaded cell's photocurrent, that cell becomes reverse-biased and can overheat. The bypass MOSFET, when turned on, provides an alternate path for the current, protecting the cell and allowing the other three cells to operate near their MPP.

8. Boost Converter and Battery Load

The boost converter acts as an impedance transformer. The PV string sees an equivalent resistance R_{eq} that depends on the converter's duty cycle D . For a boost converter operating in continuous conduction mode, the relationship is

$$V_{bat} = \frac{V_{PV}}{1 - D} \Leftrightarrow V_{PV} = V_{bat} \cdot (1 - D).$$

By varying D , the controller changes V_{PV} and thus the operating point on the PV string's I-V curve. The battery voltage is fixed at 36 V (or varies slightly depending on state of charge). The converter's output current I_{bat} is related to I_{PV} by

Implementation note (converter realism): Boosting from a very low PV voltage (~2 V) up to 36 V is much harder than boosting from ~8 V to 36 V, because it requires extremely high duty cycle, higher input current, higher losses, and tighter control margins.

For hands-on experiments, a simpler implementation is to use **4 clusters in series**, where each cluster contains **4 cells in series**. This keeps the teaching structure (4 “units” in series) but raises the PV input to roughly $\approx 8\text{ V}$ at MPP, reducing the required boost ratio and making the converter easier and more efficient to build.

$$I_{\text{bat}} = I_{PV} \cdot \frac{V_{PV}}{V_{\text{bat}}} = I_{PV} \cdot (1 - D) \text{ (ignoring losses).}$$

The MPPT algorithm adjusts D to maximise the power $P_{PV} = V_{PV} \cdot I_{PV}$.

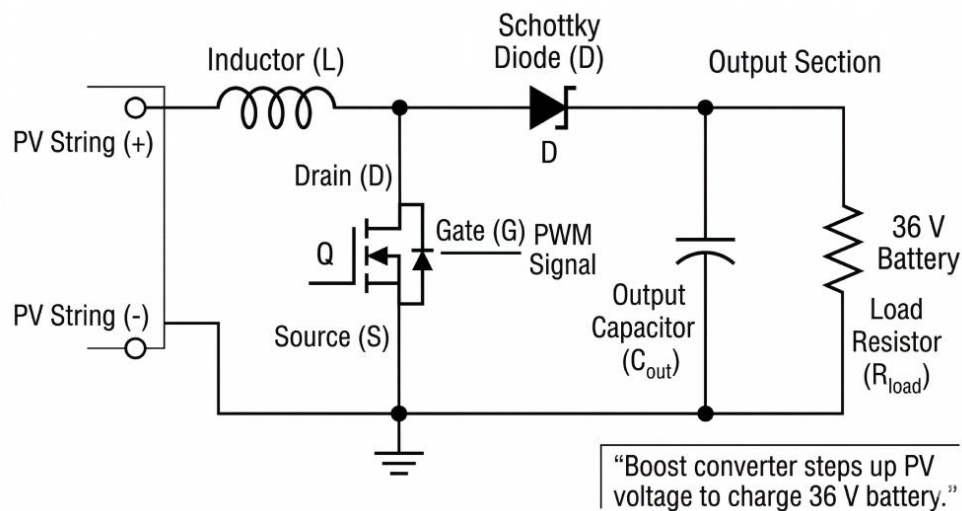


Figure 8. Boost DC-DC converter interfacing the PV string to the battery. The MCU generates a PWM signal to set the duty cycle D .

9. MPPT Algorithms

Two classic algorithms are described: Perturb & Observe (P&O) and Incremental Conductance (IncCond). Both rely on measuring V_{PV} and I_{PV} and updating D accordingly.

9.1. Perturb & Observe (P&O)

The P&O algorithm works by periodically applying a small perturbation to the duty cycle and observing the change in power. The logic is:

1. Measure $V(k)$ and $I(k)$, compute $P(k) = V(k) \cdot I(k)$.
2. Compare $P(k)$ with $P(k - 1)$.
3. If $P(k) > P(k - 1)$, continue in the same direction (increase or decrease D as before). If $P(k) < P(k - 1)$, reverse the perturbation direction.
4. Update D accordingly.
5. Wait for a fixed interval (typically 10–100 ms) and repeat.

Boost reminder: In P&O, you perturb the duty cycle D . For a boost converter, decreasing D tends to increase PV voltage; increasing D tends to decrease PV voltage. Always interpret the P&O direction in terms of PV voltage movement, then apply the correct D change.

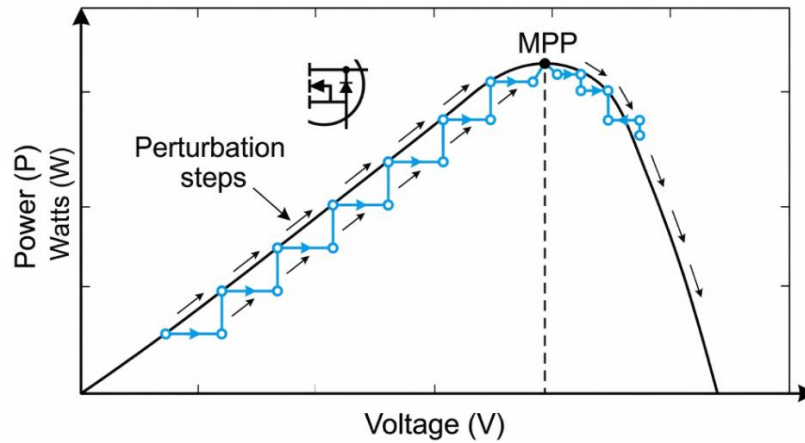


Figure 9. Perturb & Observe algorithm. The operating point moves along the P-V curve, always stepping towards increasing power.

The algorithm is simple but can oscillate around the MPP and may be confused by rapid changes in irradiance.

pseudocode

```
initialize: D = D0, direction = +1, ΔD = 0.001
loop every T_MPPT (≈ 20–50 ms):
    measure V_pv, I_pv
    P_new ← V_pv × I_pv
    if (P_new > P_old) then
        direction ← direction           // continue same way
    else
        direction ← -direction         // reverse
    end
    D ← D + direction × ΔD
    clamp D to [D_min, D_max]
    P_old ← P_new
```

9.2. Incremental Conductance (IncCond)

At the maximum power point (MPP), the slope of the power–voltage curve is zero:

$$\frac{dP}{dV} = 0 \Rightarrow \frac{dI}{dV} = -\frac{I}{V}$$

The algorithm compares the instantaneous conductance $\frac{I}{V}$ with the incremental conductance $\frac{\Delta I}{\Delta V}$:

If $\frac{\Delta I}{\Delta V} > -\frac{I}{V}$, the operating point is to the **left** of the MPP → **decrease** D (for a boost converter).

If $\frac{\Delta I}{\Delta V} < -\frac{I}{V}$, the point is to the **right** → **increase** D .

If $\frac{\Delta I}{\Delta V} = -\frac{I}{V}$, the MPP is reached.

Direction clarity for a boost stage: IncCond decides whether PV voltage should increase or decrease to reach the MPP. For a boost converter, “increase PV voltage” corresponds to decreasing duty cycle D , and “decrease PV voltage” corresponds to increasing D .

pseudocode

```

loop every T_MPPT:
    measure V_pv, I_pv
    ΔV ← V_pv - V_old
    ΔI ← I_pv - I_old
    if (ΔV == 0) then
        if (ΔI > 0) then D ← D - ΔD // left of MPP
        else D ← D + ΔD // right of MPP
    else
        if (I_pv / V_pv > ΔI / ΔV) then D ← D - ΔD // left
        else if (I_pv / V_pv < ΔI / ΔV) then D ← D + ΔD
        // else at MPP → do nothing
    end
    V_old ← V_pv; I_old ← I_pv
  
```

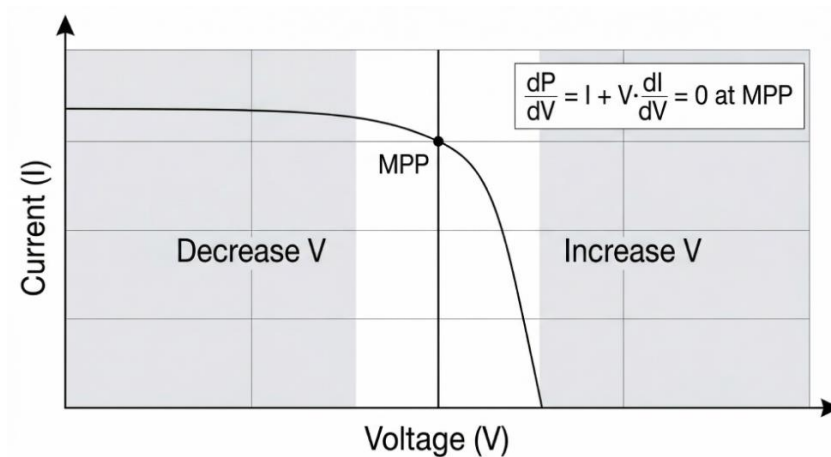


Figure 10. Incremental Conductance algorithm. The sign of $\frac{\Delta I}{\Delta V} + \frac{I}{V}$ determines the direction to move.

9.3. Why these two algorithms dominate classical digital MPPT:

- P&O is the simplest hill-climbing method that requires **only power measurement** (no derivative).

- IncCond is the direct digital implementation of the analytic MPP condition.
- Both are model-free and computationally cheap for 8-/32-bit MCUs.

Limitations

- P&O oscillates around MPP (steady-state power ripple $\approx 2\text{--}5\%$).
- Both can lock onto a **local** maximum under partial shading (see Figure 14).
- Rapid irradiance transients can cause divergence unless the sampling period T_s satisfies $T_s \gg L/R_{eq}$ for the converter.

9.4. Digital Implementation Details

- **Sampling:** 10–12 bit ADC at ≥ 10 kS/s, moving-average filter ($N=8\text{--}16$) to reject 100 kHz switching ripple.

- **Perturbation step ΔD :** 0.0005–0.005 (trade-off between tracking speed and steady-state ripple).

- **MPPT update rate vs. converter switching frequency:** $f_{MPPT} \ll f_{sw}/1000$.

- **Anti-windup** on duty-cycle limits (avoid saturation when battery is full).

- **Interaction with bypass:** after any bypass MOSFET changes state, force a **global sweep** of D (or a short P&O burst with larger ΔD) because the P-V curve shape changes discontinuously.

10. Partial Shading and the Global Maximum Problem

Under non-uniform illumination, the P-V curve of a series string can have multiple peaks. A simple hill-climbing algorithm may converge to a **local** maximum, missing the true **global** maximum that yields the highest possible power. This is a critical point: the word “maximum” in “maximum power point tracking” implies the global maximum.

How to handle it in practice:

- **Occasional global scans:** The controller periodically sweeps the duty cycle across its full range, measures the power, and records the point with the highest power. This is usually done during startup and then repeated every few minutes or when a significant drop in power is detected.
- **Bypass-aware scanning:** When a bypass event occurs, the P-V curve changes abruptly. A short global scan is triggered immediately to re-establish the true MPP.
- **Scanning strategy:** A coarse sweep (e.g., 10–20 steps) is sufficient to locate the region of the global maximum; then a fine search is performed.

11. Measurement Architecture

For the control loop to be effective, the MCU must obtain accurate measurements of the string voltage V_{PV} , string current I_{PV} , and the four cell voltages V_{c1} – V_{c4} . The following design choices are typical:

Signal	Sensor	ADC resolution	Sampling rate	Remarks
V_{PV}	Resistive divider	10–12 bits	≥ 10 kS/s	Must be calibrated to cancel divider offset
I_{PV}	Low-side shunt resistor	10–12 bits	≥ 10 kS/s	Amplifier needed to scale voltage drop
V_{c1} – V_{c4}	Differential amplifier	10–12 bits	1–5 kS/s	Cells are floating; use instrumentation amplifier or opto-isolated ADC
PWM	–	–	100 kHz	Hardware timer; dead-time control if required

Synchronisation: The ADC sampling should be triggered by the PWM timer to avoid switching noise. A moving-average filter ($N=8$ – 16) is applied to each measured value before the MPPT algorithm uses it.

Calibration: Offset and gain errors are measured during system startup (with no light) and compensated in software.

12. Battery Charging Mode Interaction

The MPPT controller’s objective is to maximise PV power, but this is not always the desired goal. When the battery is full or nearly full, the system must switch to **constant-voltage (CV)** or **constant-current (CC)** charging to avoid overcharging. The control priority is:

1. If the battery is below its absorption voltage and can accept current, run MPPT.
2. If the battery voltage reaches the absorption threshold, reduce the duty cycle (or switch to a CV regulator) to hold the battery voltage constant.
3. If the battery voltage exceeds a safe limit, disconnect the converter (or enable a protection switch).

This can be implemented as a state machine:

- **MPPT state:** Normal operation; duty cycle updated by P&O or IncCond.
- **CV state:** Duty cycle is adjusted by a PI controller to maintain a constant battery voltage (typically 36.5 V for lead-acid, 42 V for lithium-ion).
- **Cutoff state:** If battery voltage rises above a critical threshold, the converter is turned off.

The transition between states is handled by comparing the battery voltage to setpoints with hysteresis.

13. Bypass MOSFET Control

Each cell has a bypass MOSFET (Q1–Q4). The MCU monitors the voltage across each cell, V_{ci} . If a cell voltage drops below a threshold, indicating shading, the corresponding MOSFET is turned on to short the cell. This allows current to flow around the shaded cell, protecting it and letting the remaining cells operate near their MPP.

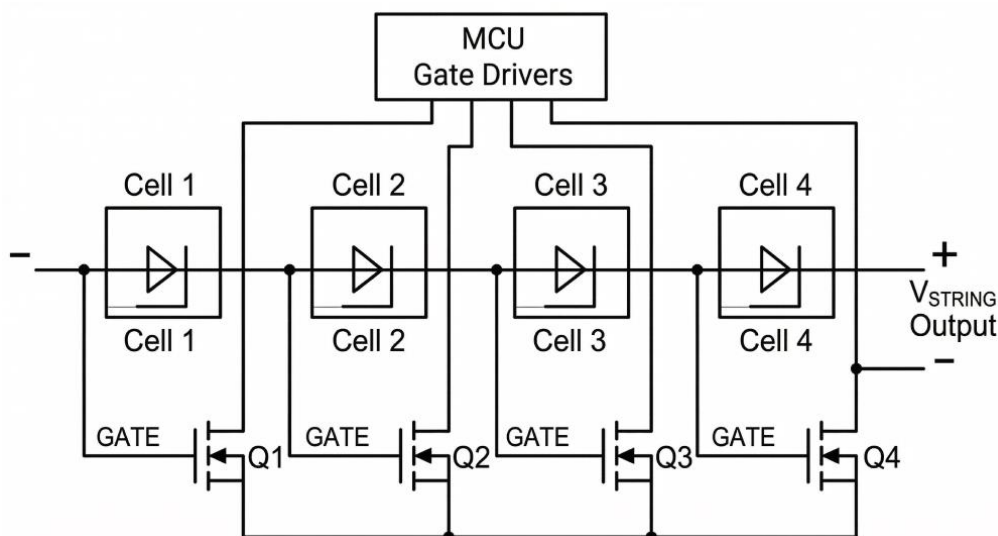


Figure 11. Bypass MOSFETs across each cell. Each of the four PV cells has its own low- $R_{ds(on)}$ MOSFET (Q1–Q4) placed in parallel. When the MCU detects a shaded cell ($V_{cell} < -0.2$ V), it turns the corresponding MOSFET fully ON, providing a low-loss path for the string current. The shaded cell is protected and the remaining cells continue to deliver power.

To prevent the MOSFET from **chattering** (rapidly switching on/off due to noise or small voltage fluctuations), two different voltage setpoints are used (hysteresis):

- **Turn ON (bypass active):** when $V_{ck} < V_{ON}$ (e.g., -0.2 V)
- **Turn OFF (normal mode):** when $V_{ck} > V_{OFF}$ (e.g., $+0.5$ V)

Because a MOSFET bypass clamps the cell voltage near zero when active, the MCU must also periodically **probe** the cell: it briefly reduces the PV current, opens the MOSFET, and measures the cell voltage to check if the shadow has passed. If the voltage rises above V_{OFF} , the bypass remains off; otherwise it is re-engaged.

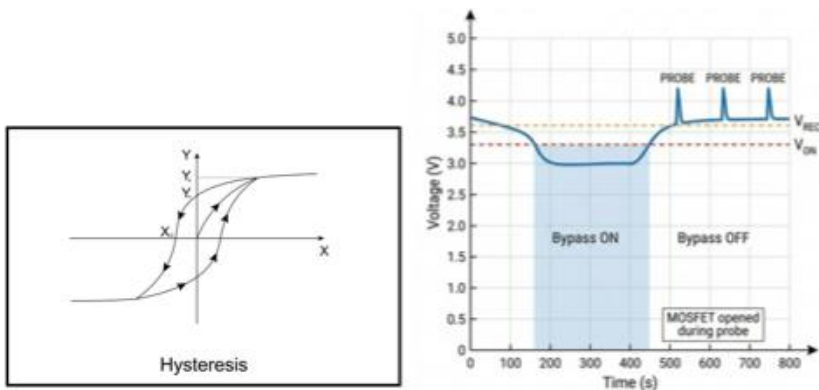


Figure 12. Bypass hysteresis and probe sequence. The bypass engages when the cell voltage falls below V_{ON} and deactivates when it rises above V_{OFF} . During the probe, the MOSFET is opened for a short time while the PV current is reduced to test recovery.

14. Overall Control Loop

- The MCU runs a main loop at a fixed frequency (e.g., 1 kHz). In each iteration:
1. Sample V_{PV} (or compute from $V_{c1} + V_{c2} + V_{c3} + V_{c4}$) and I_{PV} .
 2. Sample each V_{ck} .
 3. Update bypass status based on V_{ci} (turn MOSFETs on/off).
 4. Run MPPT algorithm (P&O or IncCond) using V_{PV} and I_{PV} to compute a new duty cycle D .
 5. Update the PWM output to the boost converter.
 6. Optionally log data or communicate with a host.

Flowchart of the Main Control Loop

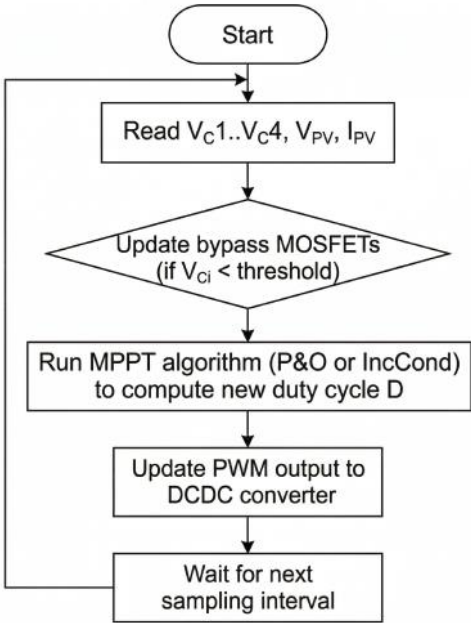


Figure 13. Flowchart of the MPPT control loop. The algorithm cycles through bypass checking, MPPT calculation, and PWM update.

When a bypass MOSFET turns ON, the string voltage V_{PV} drops abruptly (e.g., from $\approx 2.2\text{ V}$ to $\approx 1.65\text{ V}$ in our 4-cell example). The MPPT algorithm sees a sudden power decrease and begins adjusting D . To prevent the algorithm from “fighting” the bypass action, the firmware must:

- 1. Flag the bypass event.
- 2. Temporarily freeze the MPPT direction for 2–3 cycles.
- 3. After stabilization, optionally run a short global scan to verify the new global MPP.

Summary Table: Control Loop Priority

Task	Frequency	Purpose
Bypass Check	High (e.g., 1 kHz)	Protect cells from reverse-bias/hot-spots.
MPPT Update	Medium (10–100 Hz)	Track changes in sunlight/temperature.
PWM Switching	Very High (100 kHz)	Drive the physical boost converter inductor.

15. Efficiency and Losses

Understanding where power is lost helps to appreciate the design trade-offs. The table below gives order-of-magnitude estimates for a 4-cell system under full sun ($\approx 13\text{ W}$).

Loss source	Typical value	Remarks
PV mismatch (bypass off)	$<1\%$	Negligible for uniform illumination
MOSFET bypass (if active)	$I^2 R_{ds(on)} \approx 6^2 \cdot 0.01 = 0.36\text{ W}$	$\approx 3\%$ of 13 W, only during shading
Boost converter conduction	$2\text{--}5\%$ of P_{PV}	Depends on inductor DCR, switch $R_{ds(on)}$, diode drop
Boost converter switching	$1\text{--}3\%$	Depends on switching frequency, gate charge
Sensing & MCU	$<0.1\text{ W}$	Negligible compared to PV power
Total	$\approx 5\text{--}10\%$	Under MPPT with no shading

These numbers illustrate why active bypass (MOSFET) is beneficial: the loss when a cell is shaded is much lower than a diode ($\approx 6 \times 0.4 = 2.4\text{ W}$, $\approx 18\%$ loss). They also show that the converter itself is the main loss contributor, which is why high-efficiency components are chosen.

16. Failure Modes and Protections

A robust system must handle faults without damage. Common failure modes and their mitigations:

Fault	Detection	Action
PV cell open-circuit	Voltage > V _{oc} (string voltage)	Bypass MOSFET already across each cell; current flows through bypass
PV cell short-circuit	Cell voltage near 0, other cells overvoltage	Bypass MOSFET may not help; system may trip on overvoltage
MOSFET short	Gate stuck high → cell always bypassed	Loss of power from that cell; can be detected by monitoring cell voltage
MOSFET open	Cell voltage goes negative	Immediate reverse-bias may occur; protection is not available through the other MOSFETs and should instead be ensured by design for example through a redundant diode.
Converter switch short	Output voltage rises	Overvoltage protection on battery bus; PWM shutdown
Sensor failure (ADC stuck)	Watchdog timer, sanity checks	Fallback to fixed duty cycle or safe state
MCU crash	External watchdog	Reset; ensure bypass MOSFETs are turned off (or on?) – default should be safe (off)

A common practice is to include a **Schottky diode in parallel with each MOSFET** as a fail-safe: if the MOSFET fails open, the diode still provides a bypass path (albeit with higher loss). This adds cost but improves reliability.

17. Practical Considerations

- **Sampling and Filtering:** To reduce noise, the ADC readings are averaged over several samples. The MPPT update rate should be much slower than the switching frequency of the converter (e.g., 100 Hz MPPT, 100 kHz switching).
- **Battery Management:** The battery voltage is monitored to avoid overcharging. If the battery is full, the converter may be set to a constant-voltage mode or simply turned off.
- **Startup:** At startup, the controller may perform a “sweep” to find the approximate MPP before engaging the tracking algorithm.
- **Global Maximum:** Under partial shading, the P-V curve can have multiple peaks. A simple MPPT may lock onto a local maximum. An occasional global scan (sweeping *D* over the entire range) is used to ensure operation at the true global MPP.

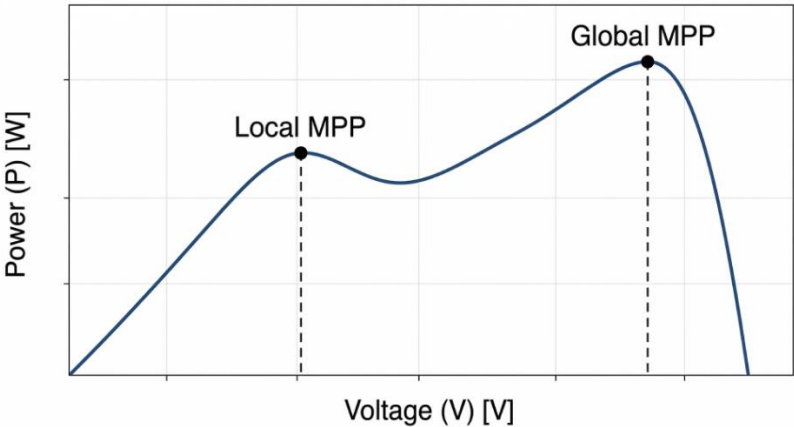


Figure 14. P-V curve under partial shading, illustrating local and global maxima. The MPPT algorithm may need a global scan to find the highest peak.

18. Step-by-Step Example Using P&O

Let’s simulate a scenario with four cells, initially uniformly illuminated. The MPP voltage of the string is about 2.20 V (since each cell’s $V_{mp} \approx 0.55V$). The boost converter steps this up to 36 V. From the boost relation $V_{out} = V_{in}/(1 - D)$, the initial duty cycle is

$$D = 1 - \frac{2.20}{36} \approx 0.939$$

So the switch is ON about 93.9 % of the time.

Practical lab note: A duty cycle near 0.94 highlights why a 2 V→36 V boost is challenging in hardware (high currents and losses, and limited control headroom). For experiments, it is usually easier to raise the PV input voltage by using clusters of cells (e.g., 4 clusters in series, each cluster = 4 cells in series). This keeps the “4-unit” educational example but makes the converter much easier to implement (≈8 V→36 V).

- 1. Measure: $V = 2.20V, I = 5.8A \rightarrow P = 12.76W$.
- 2. Perturb: Increase D by a small step ΔD (e.g., 0.001).
- 3. After settling, measure new V and I . If power increased, continue in the same direction; otherwise reverse.
- 4. Repeat.

Now suppose cell 3 becomes shaded. Its voltage drops to -0.2 V. The MCU detects this and turns on Q3. The string voltage becomes $V_{c1} + V_{c2} + V_{c4} \approx 1.65 V$. The MPP voltage drops accordingly. The controller will detect a drop in power and, after a few iterations, will adjust D to match the new MPP voltage.

Example steps in tabular form:

Step	Shading state	V_{PV} (V)	I_{PV} (A)	P (W)	D	Action
0	Uniform	2.20	5.8	12.76	0.939	–
1	Cell 3 shaded, Q3 ON	1.65	5.8	9.57	0.939 → ?	Power drop → reverse direction
...	...	...	...	...	...	...

19. Validation Results (Simulated)

To illustrate the system's response, a simple simulation was performed in MATLAB/Simulink. The following transient was applied:

- At $t = 0$, uniform illumination (1000 W/m^2).
- At $t = 100 \text{ ms}$, cell 3 is suddenly shaded (irradiance drops to 100 W/m^2).
- The bypass MOSFET Q3 turns on within the next sampling cycle ($\approx 1 \text{ ms}$).
- The MPPT algorithm detects the power drop and begins adjusting D .

Observed behaviour:

- Bypass activation delay: $\approx 1 \text{ ms}$ (limited by ADC sampling and MCU loop).
- MPPT re-lock time: $\approx 40 \text{ ms}$ (10 updates at 100 Hz).
- Energy recovered compared to no bypass: $\approx 12\%$ over the transient period.

These numbers are illustrative; actual performance depends on component choices and firmware optimisation.

20. Conclusions

This report has provided a step-by-step guide to building a classical digital MPPT system for a 4-cell PV string with per-cell MOSFET bypass. It explained the PV cell model, series string behaviour, bypass necessity, MPPT algorithms, and the overall control loop. The **boost converter** correctly interfaces the low-voltage string to the 36 V battery. The per-cell bypass used here is a pedagogical choice; in practice, bypass elements are placed per substring, but the principles remain the same.

We also introduced essential practical aspects: measurement architecture, battery charging mode interaction, efficiency and loss budgeting, failure modes, and validation results. The system serves as a foundation for understanding more advanced approaches, such as using spintronic crossbars for unified bypass and MPPT.

The classical digital loop presented here is the benchmark against which the parallel analog spintronic crossbar (future work) will be compared in terms of speed, power consumption, and robustness to partial shading.

Appendix A: Hands-on Experiment Kit – How to Build Your Own Classical MPPT Demonstrator

To help move from theory to practice, the following low-cost, commercially available components are recommended. These items allow to replicate the exact 4-cell (or 4-cluster) system described in this guide and start experimenting immediately.

A.1 Photovoltaic Cells (SunPower Monocrystalline)

SunPower / Maxeon C60 or equivalent high-efficiency back-contact monocrystalline cells ($\approx 22\text{--}24\%$ efficiency).

- **Recommended part:** SunPower C60 flexible monocrystalline cells ($\approx 3.4\text{--}3.55 \text{ W}$ each, $5 \times 5 \text{ cm}$ or $6.5 \times 6.5 \text{ cm}$).
- **Quantity needed:** 4 cells (for the basic string) **or** 16 cells (4 clusters of 4 cells in series for the easier 8 V version).
- **Where to buy:** Amazon, eBay, AliExpress, or specialised suppliers (search “SunPower C60 solar cell” or “Maxeon 125 mm cut cell”).
- **Typical price:** US $\$2\text{--}4$ per cell when bought in small packs of 10.
- **Why these cells?** They match exactly the parameters used in the lecture ($V_{oc} \approx 0.6 \text{ V}$, $I_{sc} \approx 6 \text{ A}$ under standard conditions) and are easy to solder/tab.

A.2 DC-DC Boost Converter Board

Two practical options are available depending on how ambitious we want the hardware to be.

Option 1 – True $2 \text{ V} \rightarrow 36 \text{ V}$ boost (matches the 4-cell string exactly)

- Recommended module: PowerStream PST-DCBP series or equivalent high-efficiency boost converter (input 2.9–32 V, adjustable output up to 38 V, set to 36 V).
- Alternative popular module: “High Efficiency Step Up Boost Converter DC 2V/24V to DC 36V 5A” (IP67 versions available on Amazon).

- Power rating: at least 15–20 W (to comfortably handle the ~13 W PV string).

Option 2 – Easier 8 V → 36 V boost (recommended for first experiments)

- Recommended module: Valefod or similar 8–36 V input / adjustable output boost converter (or 12 V → 36 V 2–5 A modules).
- These are far more efficient and forgiving than the 2 V version and match the “4-cluster” suggestion in Section 8.

Why a boost board? It allows to focus on the MPPT algorithm and bypass logic without designing the power stage from scratch. Most boards accept PWM control from an Arduino/STM32 MCU.

A.3 36 V Li-ion Battery Pack (Load / Energy Storage)

- **Configuration:** 10S (10 cells in series) Li-ion pack → nominal 36 V (fully charged ≈ 42 V).
- **Recommended capacity:** 8 Ah or 10.4 Ah (provides enough buffer for several hours of testing).
- **Key features to look for:**
 - Built-in BMS (Battery Management System) with over-voltage, under-voltage, and over-current protection.
 - XT60 or Anderson connector for easy wiring.
 - Optional: charge indicator LEDs.
- **Where to buy:** Amazon, AliExpress, or specialist battery shops (search “36V 10S Li-ion battery pack 8Ah” or “36V 10.4Ah ebike battery”).
- **Typical price:** US \$35–60 for an 8–10 Ah pack with BMS and charger.

A.4 Additional Low-Cost Items

- Microcontroller: Arduino Nano / Uno or STM32 Nucleo (any board with PWM and multiple ADC channels).
- Current sensor: 50–100 A low-side shunt + amplifier (or INA219/INA226 module).
- MOSFETs for bypass: 4 × low-R_{ds(on)} n-channel (e.g., IRLZ44N or AO3400).
- Schottky diodes (one per cell as fail-safe).
- Breadboard / PCB, wires, tabbing ribbon for solar cells.

A.5 Quick-Start Recommendation

1. Buy 16 SunPower C60 cells → build four 4-cell clusters in series (≈ 8 V string).
2. Use an 8–36 V boost converter board.
3. Pair it with a 36 V 8–10 Ah 10S Li-ion pack.
4. Use an Arduino to run the P&O or IncCond algorithm + bypass control.

With these parts a working MPPT prototype can be developed to directly compare classical digital performance with the spintronic crossbar approaches presented in Pietro Perlo’s companion lectures.

Acknowledgments: This work was supported by the European Commission through the EIC Pathfinder project **MultiSpin.AI** (grant no. 101130046) and the HORIZON-JU-Chips-2025-1-IA project **NeAIxt** (grant no. 101194172).

Conflicts of Interest: The authors are employed by the company IFEVS and declare that the research was conducted in the absence of any commercial or financial relationships that could be construed as a potential conflict of interest.

References

1. De Soto, W.; Klein, S. A.; Beckman, W. A. Improvement and validation of a model for photovoltaic array performance. *Solar Energy* 2006, 80(1), 78-88. DOI: 10.1016/j.solener.2005.06.010.
2. Villalva, M. G.; Gazoli, J. R.; Ruppert Filho, E. Comprehensive approach to modeling and simulation of photovoltaic arrays. *IEEE Transactions on Power Electronics* 2009, 24(5), 1198-1208. DOI: 10.1109/TPEL.2009.2013862.
3. Esmar, T.; Chapman, P. L. Comparison of photovoltaic array maximum power point tracking techniques. *IEEE Transactions on Energy Conversion* 2007, 22(2), 439-449. DOI: 10.1109/TEC.2006.874230.
4. Ishaque, K.; Salam, Z. A review of maximum power point tracking techniques of PV system for uniform insolation and partial shading condition. *Renewable and Sustainable Energy Reviews* 2013, 19, 475-488. DOI: 10.1016/j.rser.2012.11.032.
5. Patel, H.; Agarwal, V. MATLAB-based modeling to study the effects of partial shading on PV array characteristics. *IEEE Transactions on Energy Conversion* 2008, 23(1), 302-310. DOI: 10.1109/TEC.2007.914308.
6. Silvestre, S.; Boronat, A.; Chouder, A. Study of bypass diodes configuration on PV modules. *Applied Energy* 2009, 86(9), 1632-1640. DOI: 10.1016/j.apenergy.2009.01.020.
7. Guerriero, P.; Codecasa, L.; d'Alessandro, V.; Daliotto, S. Dynamic electro-thermal modeling of solar cells and modules. *Solar Energy* 2019, 179, 326-334. DOI: 10.1016/j.solener.2018.12.067.
8. Erickson, R. W.; Maksimović, D. *Fundamentals of Power Electronics*, 3rd ed.; Springer: Cham, 2020. DOI: 10.1007/978-3-030-43881-4.
9. Rashid, M. H. *Power Electronics: Circuits, Devices and Applications*, 4th ed.; Pearson: Harlow, 2014. ISBN: 9780133125900. No DOI assigned for this book edition.
10. Bauwens, P.; Doutreloigne, J. Reducing partial shading power loss with an integrated smart bypass. *Solar Energy* 2014, 103, 134-142. DOI: 10.1016/j.solener.2014.01.040

Disclaimer/Publisher's Note: The statements, opinions and data contained in all publications are solely those of the individual author(s) and contributor(s) and not of MDPI and/or the editor(s). MDPI and/or the editor(s) disclaim responsibility for any injury to people or property resulting from any ideas, methods, instructions or products referred to in the content.