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Article

Gate Oxide and Back Oxide Materials Combined Influence on Self-Heating and Dibl Effects in 2D MOS₂ Based Mosfet

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Abstract: In this paper, degradation effects such as the self-heating effect and the DIBL effect in 2D-MoS₂-based MOSFETs are investigated through simulations. These are transistors with Al₂O₃ and HfO₂ as the gate oxide and SiO₂ and HfO₂ as the back oxide (BOX). The self-heating effect (SHE) is simulated using the thermodynamic transport model. The dependence of the DIBL effect (Drain Induced Barrier Lowering) and the lattice temperature in the center of the channel on the gate length for transistors with different gate oxide and BOX materials is considered. Transistors are considered where the channel is fully and partially (just below the gate) covered by a gate oxide. It is shown that the transistors with Al₂O₃ as gate oxide and SiO₂ as BOX materials have higher immunity to the DIBL effect and transistors with HfO₂ as gate oxide and HfO₂ as BOX materials have higher immunity to SHE.

Keywords: self-heating effect; DIBL effect; lattice temperature; thermal conductivity; 2D-MoS₂

1. Introduction

In accordance with some viewpoints, the scaling of silicon-based planar MOSFETs has approached its physical limit. However, according to some other proposals, there is still a possibility to continue the size scaling of MOSFETs by modifying the structure or by choosing the appropriate functional materials. In this context, intensive research has been carried out to find alternative transistor structures and channel materials for future devices. One of the promising materials that can be used as a channel material are transition metal dichalcogenides (TMD), especially two-dimensional TMDs. Among the other materials, transition metal dichalcogenides have shown great potential in device applications due to their satisfactory bandgaps, thermal stability, carrier mobility, and compatibility with silicon CMOS processes [1]. MOSFETs with channels based on two-dimensional materials, namely TMDs, offer superior electrostatics and excellent immunity to short-channel effects [2]. One of the most popular TMD materials considered as a channel material in MOSFETs and also considered to be the most thorough and widely studied material is two-dimensional molybdenum disulfide (MoS₂) [3–5].

In addition to short channel effects, the two-dimensional channel MOSFET surrounded by dielectrics is subject to the influence of the self-heating effect (SHE) [6–8]. The effect of temperature on the drain current of SL- MoS2-based MOSFETs is investigated in references [9,10]. It has been observed in [10] that the drain current decreases, but not very sharply, with increasing temperature, while at the same time the subthreshold drain current increases quite sharply. The disadvantages of such devices mentioned above can be the fact that both the top and the bottom of the transistor channel have borders with oxide layers. The top/upper oxide layer is the gate oxide layer and the bottom oxide layer is the BOX layer. Due to the low thermal conductivity of the oxides, this condition

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can cause the heat flow from the channel to decrease during current flow compared to bulk MOSFETs, resulting in channel self-heating, which affects the transistor's characteristics. Therefore, in order to realize high-performance MOSFETs based on MoS₂ or some other transition metal dichalcogenides, important problems such as SHE must also be solved.

As mentioned above, a 2D MoS2-based MOSFET has sufficient immunity against short channel effects, especially against the DIBL effect [2]. The value of this effect depends on the electrical parameters of the channel material and the materials surrounding the channel, in particular, the dielectric constant of these materials [11]. However, thermal parameters of the same materials, namely thermal conductivity and heat capacitance, are responsible for thermal properties or thermal effects in the transistor. Therefore, to study the transistor immunity to degradation effects such as the DIBL effect and self-heating effect, the combined influence of materials surrounding the channel on the above effects must be considered. In this context, the SHE and DIBL effect for 2D MoS2-based MOSFETs with the most commonly used oxide materials such as SiO2, Al2O3, and HfO2 as gate oxide and back oxide (BOX) in different combinations is considered in this work to compare them. Due to the small dielectric constant, SiO2 is not considered as a gate oxide in this work...

2. Simulation conditions and parameterization of the transistor

The advanced TCAD tool Sentaurus is used to simulate the device numerically. Since 2D-MoS₂ is not included in the standard version of Sentaurus, the parameters, and models proposed in [2] were implemented. The thermodynamic transport model and the doping-dependent mobility model including velocity saturation at high field were used. To calibrate the model used, simulation results of the Id-Vg characteristics were compared with results in [2]. The comparison made for different gate lengths of the transistor (see Figure 1) has led to a good agreement.

The channel of the simulated transistor consists of a MoS2 monolayer with a thickness of 0.65 nm, and the total length of the device varies between 60 and 300 nm depending on the gate length. For all considered transistors, the channel length is 3 times longer than the gate length. The aluminum gate has a thickness of 2 nm and lengths of 20, 30, 50, and 100 nm. The charge carrier mobility in the channel depends on the gate oxide material, so we used a mobility of $125 \text{ cm}^2/(\text{V} \cdot \text{s})$ for Al_2O_3 [2] and $320 \text{ cm}^2/(\text{V} \cdot \text{s})$ for HfO_2 [12] gate oxide.

In order to define the optimal structure of the transistor to increase the immunity against degradation effects, the SHE and DIBL effects for a MOSFET with 2D-MoS2 as a transistor channel are simulated in two cases (see Figure 2). In the first case the length of the gate oxide is equal to the length of the MoS2 channel (Figure 2a), and in the second case, the length of the gate oxide is equal to the gate length (Figure 2b).

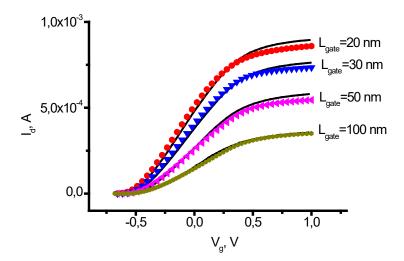


Figure 1. Ig-Vd characteristics of the considered 2D-MoS₂ based MOSFETs with different gate lengths. The gate oxide material is Al₂O₃, and the BOX material is SiO₂ with thickness 1020 nm. The Drain

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voltage is Vd = 1V. Solid lines correspond to results in [2], and colored marks are the results of our simulation.

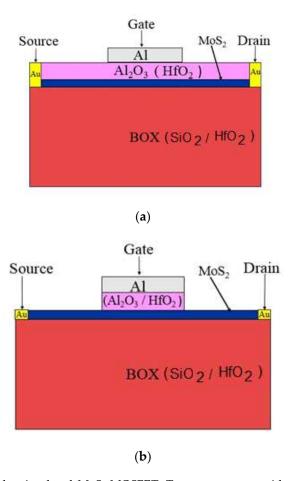


Figure 2. Structure of the simulated MoS₂ MOSFET. Two cases are considered: (a) the gate oxide length is equal to the MoS2 channel length (first case); (b) the gate oxide length is equal to the gate length (second case).

The simulation is performed by considering the four combinations of the gate oxide and back oxide materials (see Table 1). An equivalent gate oxide thickness of 1nm is chosen for all combinations, therefore SiO₂ based gate oxide is not considered to rule out a possible gate leakage current at such thicknesses. Since Al₂O₃ is practically not used as a back oxide material in MOSFETs, Al₂O₃ is also not considered as a back oxide material in this work. The thickness of the BOX layer is 80 nm.

Table 1. Combination of the gate and back oxide materials considered in the work.

Number of combination	Gate oxide	Back oxide material
	material	
1	Al ₂ O ₃	SiO ₂
2	HfO_2	SiO ₂
3	Al ₂ O ₃	HfO_2
4	HfO_2	HfO ₂

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3. Simulation results and discussion

3.1. SHE for different combinations of the gate oxide and back oxide materials

One of the main parameters of the SHE effect is the temperature in the center of the channel. The resulting temperature in the center of the channel is defined by the heat generation rate as well as by the rate of heat dissipation through the oxide surrounding the channel. In this study we will consider only heat dissipation, having assumed that heat generation is similar in all considered cases. The heat generation rate similarity is provided by choice of the appropriately applied voltages at the gate and drain. The resulting heat dissipation rate is defined by the configuration of the transistor structure as well as by oxide materials surrounding the channel. Different structures are presented in the two cases mentioned above and using the different oxide materials are presented by combinations from 1 to 4 shown in Table 1.

Results of the simulation of the lattice temperature dependence of the gate length in the middle of the channel for case 1 (fully covered channel top surface) and case 2 (partly covered channel top surface) are shown in Figure 3.

For all transistors, the temperature distributions along the channel were simulated at the same gate and drain voltages: Vg = 0.1 V and Vd = 0.4 V. Therefore, the drain current for long-channel transistors is lower than for short-channel transistors. This results in a higher mid-channel temperature for short-channel transistors than for long-channel transistors. The more significant role of the heat generation rate is observed in short-channel transistors, where for both cases and for both combinations the mid-channel temperature is higher. Besides it, the resulting temperature depends on the transistor structure (case 1 and case 2) as well as gate oxide and BOX materials (combinations 1 and 4).

With a gate length of less than 40 nm, for both 1 and 4 combinations, the lattice temperature in the middle of the channel is slightly higher for transistors where the channel is covered by gate oxide only under the gate (second case) than for transistors of the first case. This is related to the quite different thermal conductivity of gate oxide materials and air covering the upper part of the channel not covered by the gate oxide (Figure 2b). The thermal conductivity of air has values from 0.0084 to 0.0915 W/(m K) in the temperature range between 90 K and 1473 K, the thermal conductivity of aluminum- and hafnium- oxide is 2-3 orders of magnitude higher than the thermal conductivity of air.

At increasing a gate length of more than 40 nm relations between a mid-channel temperature for transistors of combination 1 and 4 are changed: the temperature for transistors of the first case became higher than for transistors of the second case. It means that the impact of oxide materials in establishing the resulting temperature is increased for the long-channel transistors in comparison with the impact of the transistor structure.

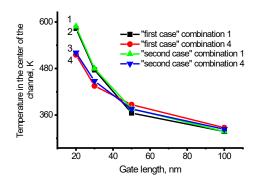


Figure 3. Dependence of the lattice temperature in the center of the channel on the gate length for 2D MoS₂ MOSFETs with Al₂O₃ gate oxide (1.2) and HfO₂ gate oxide (3.4) with fully covered channel top (2.4) and with partially covered channel top (only under the gate) (1,3).

For long-channel transistors, the temperature tends to be equal for all types of considered transistor structures. In this case, it is observed more sufficient role in the heat generation rate than the heat dissipation rate, because the resulting temperature is the same for both structures (case 1 and case 2) and for both combinations 1 and 4.

From the distribution of temperature along the channel for case 1 (fully covered channel top surface) and case 2 (partly covered channel top surface), it can be seen that with the same applied gate and drain voltages, the temperature change, when lengthening the gate, is higher for the transistor with combination number one than for transistor with combination number four (Table 1) (Figures 4 and 5). Change of the mid-channel temperature is 264 K and 192 K in the first case, 270 K and 194 K in the second case for combinations 1 and 4 respectively. Apparently, this is mainly related to the higher thermal conductivity of Al₂O₃ compared to the thermal conductivity of HfO₂ (Table 2) used as gate oxide materials in combinations number one and four, respectively. Although the thermal conductivity of SiO₂ used as BOX material in transistor combination number one is lower than the thermal conductivity of HfO₂ used as BOX material in combination number four, it differs only by 1.5- fold, while the thermal conductivity of gate oxide materials is approximately five times different (Table 2).

The results of simulating for more range of oxide materials combinations show that SHE is sensitive to gate length for all combinations of the gate and back oxide materials in the transistor

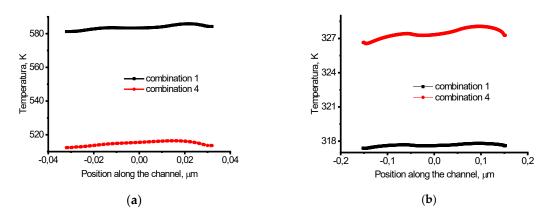


Figure 4. Distribution of temperature along the channel for the "first case" transistors with gate oxide materials Al_2O_3 and HfO_2 , while BOX material is SiO_2 and HfO_2 respectively. The gate lengths is 20 nm (a) and 100 nm (b). Vg=0.1 V and Vd=0.4 V.

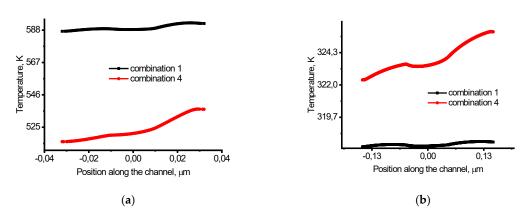


Figure 5. Distribution of temperature along the channel for the "second case" transistors with gate oxide materials Al₂O₃ and HfO₂, while BOX material is SiO₂ and HfO₂ respectively. The gate lengths are 20 nm (a) and 100 nm (b). Vg=0.1 V and Vd=0.4 V.

(Figure 6). SHE dependence on the gate length was shown only for transistors for the first case because, for transistors for the second case, this dependence is the same. The sensitivity of SHE to the gate length can be explained by the dependence of the drain current on the channel length, which in our analysis is based on the gate length. As the drain current defines the heat generation rate, therefore at a long channel (small drain current) heat generation rate is decreased. Decreasing the temperature at long channel transistors and their tendency to the equal value for all four oxide materials combinations prove our assumption that at long channel main mechanism responsible for establishing the resulting temperature is the heat generation rate.

Thermal conductivity Thickness Material (nm) $K \cdot m$ SiO_2 80 1.4 2.385 12 Al₂O₃ HfO2 (gate oxide) 5.641 2.3 80 2.3 HfO₂ (Box)

Table 2. Thermal conductivities used for simulation.

The temperature behavior shown in Figure 6 can be explained by heat dissipation through the gate oxide and BOX. It is known that low thermal conductivity of oxide materials results in slow heat dissipation from the material and the formation of a higher equilibrium temperature in the channel than materials with higher thermal conductivity. Therefore, the thermal conductivity of the gate oxide and BOX materials is a very important parameter in SHE. Depending on the thickness, the thermal conductivity of HfO2 films has values in the range of 0.27–4.3 W/(m·K) at temperatures between 300 and 500 °C [13,14]. The values of the thermal conductivity of SiO2 are in the range between 0.5 and 2.5 W/(m·K) [15,16]. In our simulation, we used thermal conductivity values presented in Table 2.

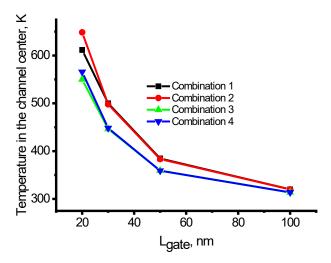


Figure 6. SHE dependence on the gate length for different combinations of gate oxide and back oxide materials.

For gate lengths up to 100 nm, the mid-channel temperature for transistors with combinations 1 and 2 is higher than for combinations 3 and 4. Apparently, it is mainly related to a lower thermal conductivity of BOX material in combinations 1 and 2 (which is SiO₂) than the thermal conductivity of BOX material in combinations 3 and 4 (which is HfO₂). With the smallest gate length, lower than

30 nm, for combinations with the same BOX material, the temperature is higher for combinations with HfO₂ as gate oxide material. At gate lengths between 30 and 100 nm the temperature for combinations with the same BOX material approximately is the same and practically is not dependent on the gate oxide material. At gate length 100 nm the temperature practically is the same for all four combinations, which means that at high gate lengths, the resulting mid-channel temperature is not dependent on gate oxide as well as BOX materials.

3.2. DIBL effect for different combinations of the gate oxide and back oxide materials

DIBL effect is one of the degradation effects similar to SHE, and also depends on the properties of the gate oxide and BOX materials. For comparison with the SHE effect, it was simulated the DIBL effect in a MOSFET with 2D-MoS2 as a transistor channel, with mentioned above four combinations of the gate oxide and BOX materials.

The results of the simulating show that the DIBL effect is more sensitive to the gate length for transistors with combinations number 3 and 4 (Figure 7). In all ranges of transistor length, the DIBL effect for transistors with combination numbers 3 and 4 is higher than for transistors with combination numbers 1 and 2. It can be understood in the framework of a simple model describing the capacitive coupling between the channel and the device electrodes, proposed in [11]. Within this model, the modified expression for DIBL given by [11] is expressed as follows:

$$DIBL = \frac{c_D}{c_G} \tag{1}$$

where C_D and C_G are drain-channel and gate-channel capacitances, respectively (Figure 8). Three components contribute to the drain-channel capacitance (C_D): the capacitive coupling through the channel layer ($C_{D,ch}$), the coupling through the BOX ($C_{D,ox}$), and the coupling through the gate oxide ($C_{D,ox}$). Therefore the drain-channel capacitance (C_D) is expressed as follows:

$$C_D = C_{D.ch} + C_{Dbox} + C_{Dox} \tag{2}$$

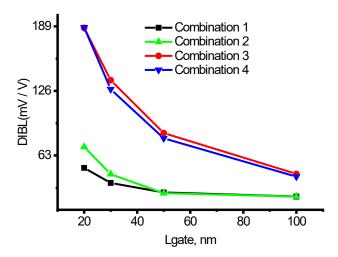


Figure 7. DIBL effect dependence on the gate length for different combinations of gate oxide and back oxide materials.

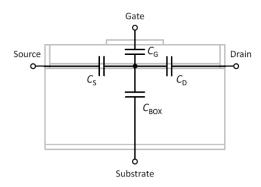


Figure 8. Equivalent circuit describing the capacitive coupling between the channel and the device electrodes in MOSFET.

The capacitance components C_G , $C_{D,ch}$, $C_{D,ox}$ depend on the dielectric constant of the channel ε_{MoS2} , while $C_{D,box}$ is unaffected by ε_{MoS2} and depends on the dielectric constant of the BOX material. In our case $\varepsilon_{Al_2O_3}=9.3$, $\varepsilon_{SiO_2}=3.9$ and $\varepsilon_{HfO_2}=25$ and $\varepsilon_{MoS_2}=3$. Therefore, because the dielectric constant of the channel ε_{MoS_2} is smaller, the gate capacitance C_G and the components $C_{D,ch}$ and $C_{D,ox}$ are reduced, and the DIBL effect is mainly will defined by $C_{D,box}$. Thus, for the transistors considered, the DIBL effect is defined by the dielectric constant of the BOX. In the range of combinations, the BOXs with the highest dielectric constant are in combinations number 3 and 4, where oxide materials are HfO_2 with $\varepsilon_{HfO_2}=25$, so these combinations should have the highest DIBL effects. Therefore, while for transistors with combinations 1 and 2, the BOX material is SiO_2 with a lower dielectric constant ($\varepsilon_{SiO_2}=3.9$); the DIBL effect for these transistors should have a lower DIBL effect. These proposals are in perfect agreement with Figure 7. It is also seen in Figure 7, that the gate oxide material does have not a considerable impact on the DIBL effect in the considered range of gate length.

3.3. Optimization of the combination of gate oxide and back oxide materials

The dependence of the lattice temperature and DIBL on the combination number carried out from Figures 6 and 7 for a gate length of 20 nm is shown in Fig 9. From the figure it can be seen that the SHE and the DIBL effect compete for the combination of the oxide materials. As we discussed above, the electrical and thermal properties of the back oxide material, namely the dielectric constant and the thermal conductivity, are mainly responsible for the SHE and DIBL effect in considered 2D-MoS₂ MOSFETs.

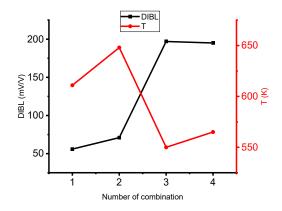


Figure 9. DIBL effect and SHE dependence of combinations of gate oxide and back oxide materials in MOSFET.

Achieving the small DIBL effect requires a low dielectric constant back oxide material, while reducing the lattice temperature in the transistor channel requires a high thermal conductivity back oxide material. We have considered two popular, conventional used, back oxide materials: SiO₂ and

HfO₂. When the back oxide material is SiO₂, it has a small dielectric constant, which leads to a reduction in DIBL; at the same time, it also has a small thermal conductivity, which leads to a high lattice temperature in the transistor channel. Conversely, in the case of using HfO₂ material as the back oxide material, it has a high dielectric constant, leading to a high DIBL effect, while it also has a relatively high thermal conductivity, leading to a reduction in the lattice temperature in the transistor channel.

From our results, it is possible to define the range of electrical and thermal parameters of oxide materials that allow reaching optimal immunity against SHE as well as the DIBL effect. From Figure 7, it can be concluded that using the BOX material with a relative dielectric constant in the range between 3.9 and 25, and with thermal conductivity between 1.4 and 2.3 W/Km is the choice for reaching optimal immunity. to degradation effects such as SHE and DIBL effects in 2D-MoS2-based MOSFET. Gate oxide material practically does not considerably impact to SHE as well as to DIBL effect.

4. Conclusions

Results of the simulation of the SHE effect show that at gate length less than 40 nm more impact to the resulting temperature has considered structures of the transistor (case 1 and case 2) than the impact of the gate oxide and BOX materials. SHE is sensitive to gate length for all combinations of the gate and back oxide materials in the transistor, while the DIBL effect is more sensitive to the gate length for transistors with combinations number 3 and 4.

The simulation results show that the choice of the gate oxide and BOX materials to ensure the possible highest immunity against degradation effects like SHE and DIBL effects is a task of optimization in 2D-MoS₂-based MOSFETs. It is shown that the electrical and thermal properties of the back oxide material have the main influence on the SHE and DIBL effects in 2D-MoS₂-based MOSFETs. Namely, using the BOX material with a relative dielectric constant in the range between 3.9 and 25, and with thermal conductivity between 1.4 and 2.3 W/(K·m) allow reaching optimal immunity against degradation effects such as SHE and DIBL effects in 2D-MoS₂ based MOSFET. Gate oxide material practically does not considerably impact to SHE as well as to DIBL effect..

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